



MT8820A WCDMA UE 测试应用

WCDMA 测试功能模块构成

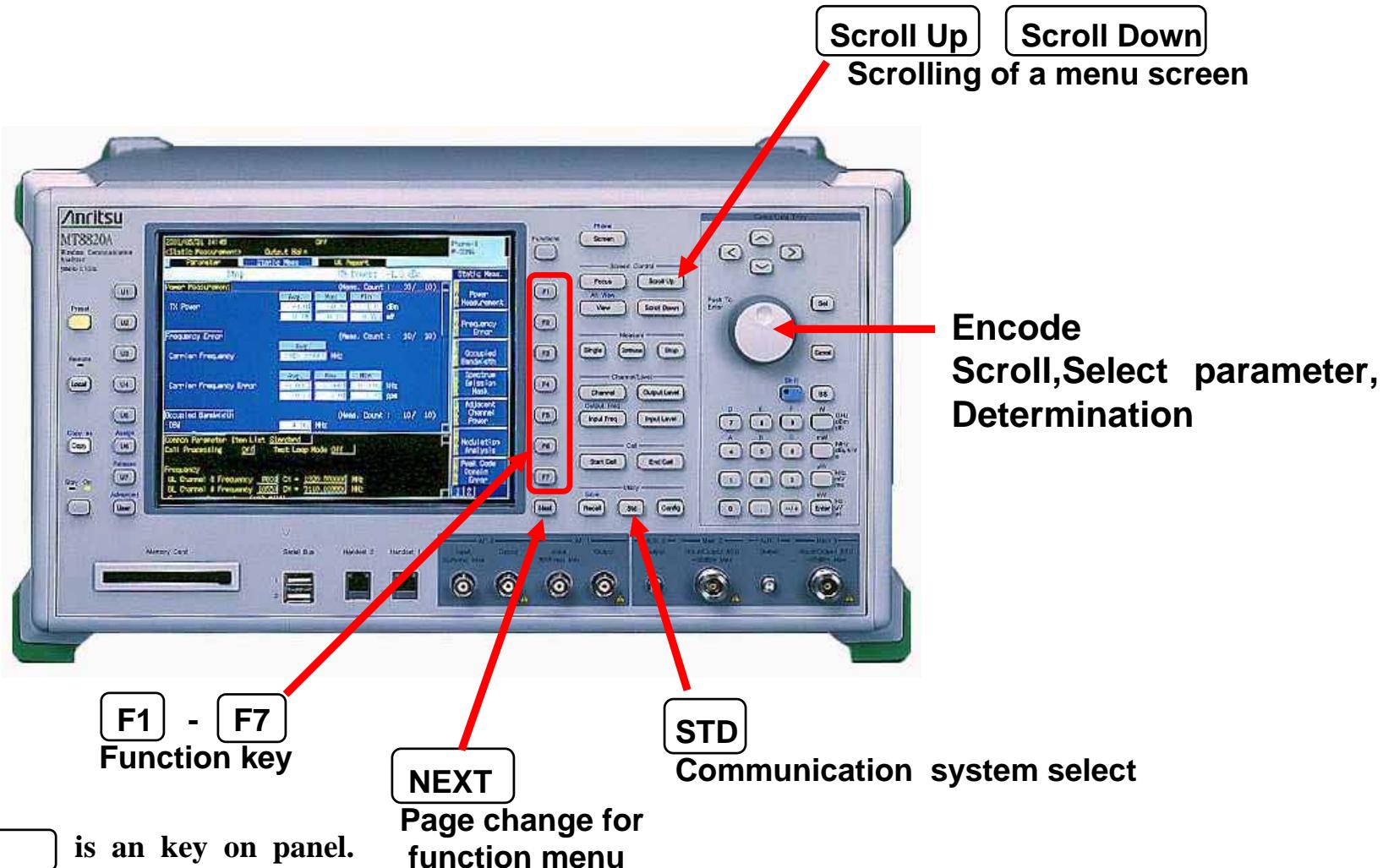


MT8820A WCDMA测试应用：

- MT8820A面板操作；**
- MT8820A一般参数设置；**
- MT8820A相关参数设置说明；**
- MT8820A WCDMA测试步骤；**

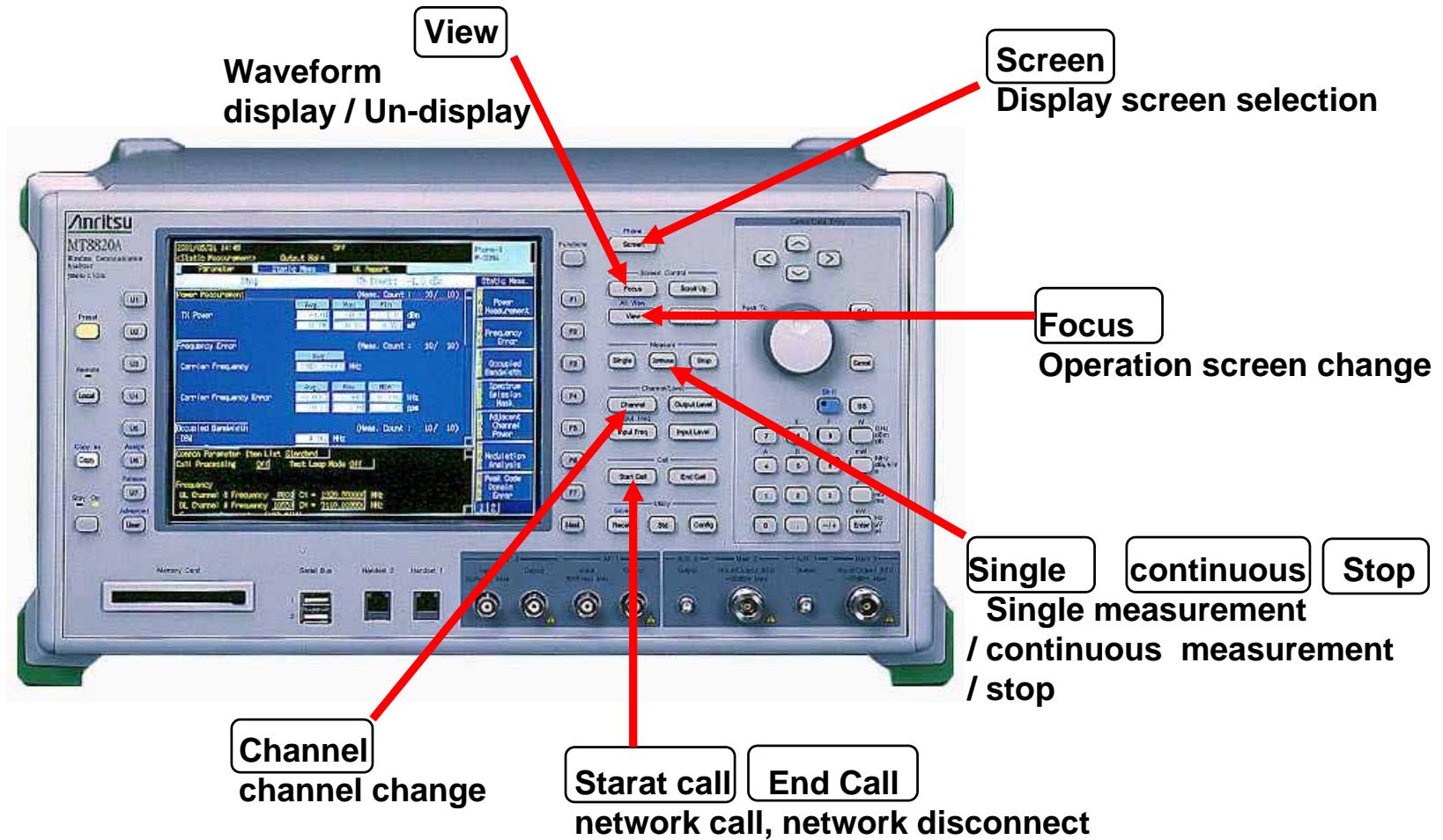
MT8820A面板操作

面板



*following is an key on panel.

面板



面板操作

Focus ---菜单切换

Parameter : 参数设置

Fundamental : 结果数据

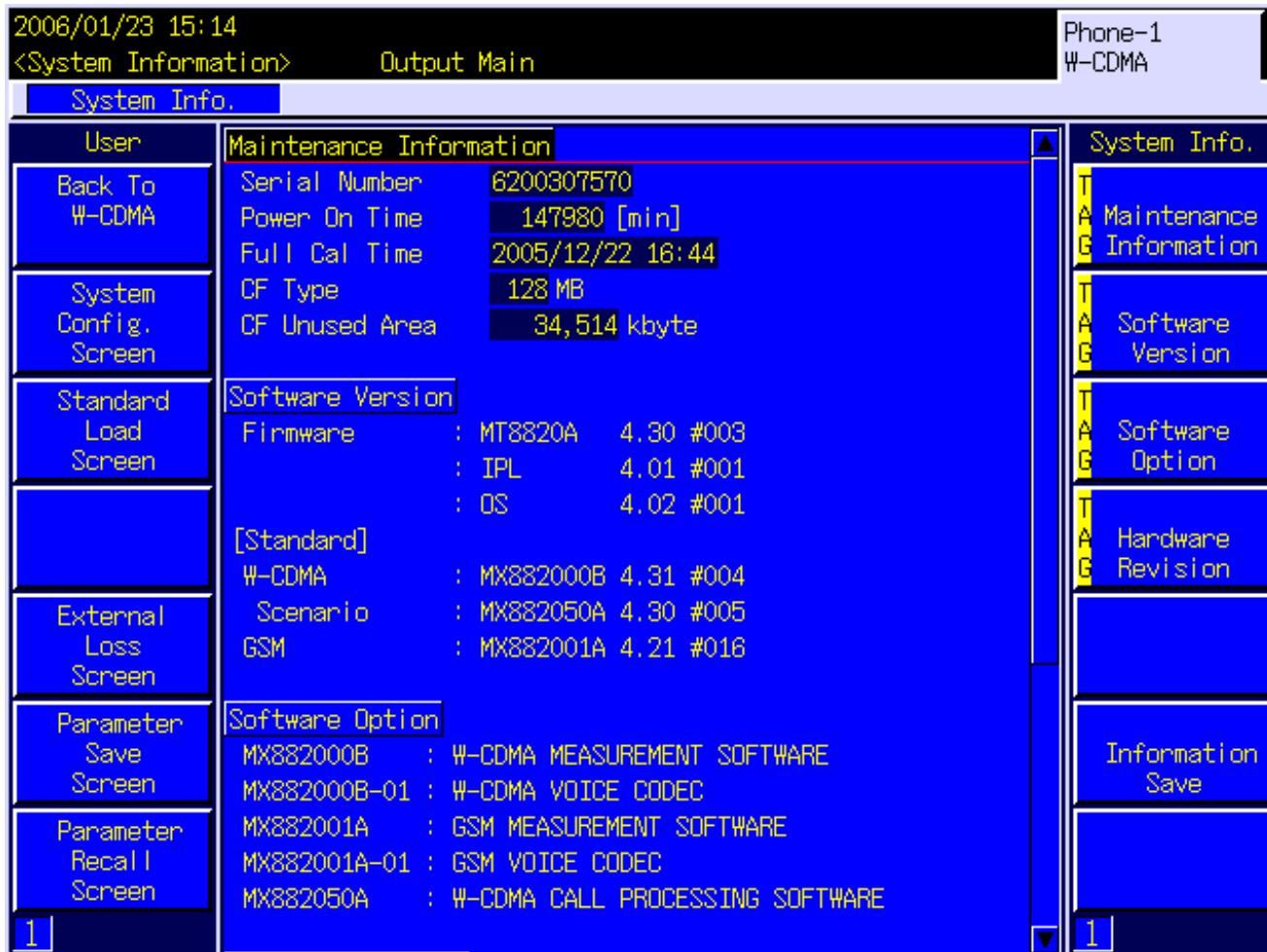
UE report : 手机汇报

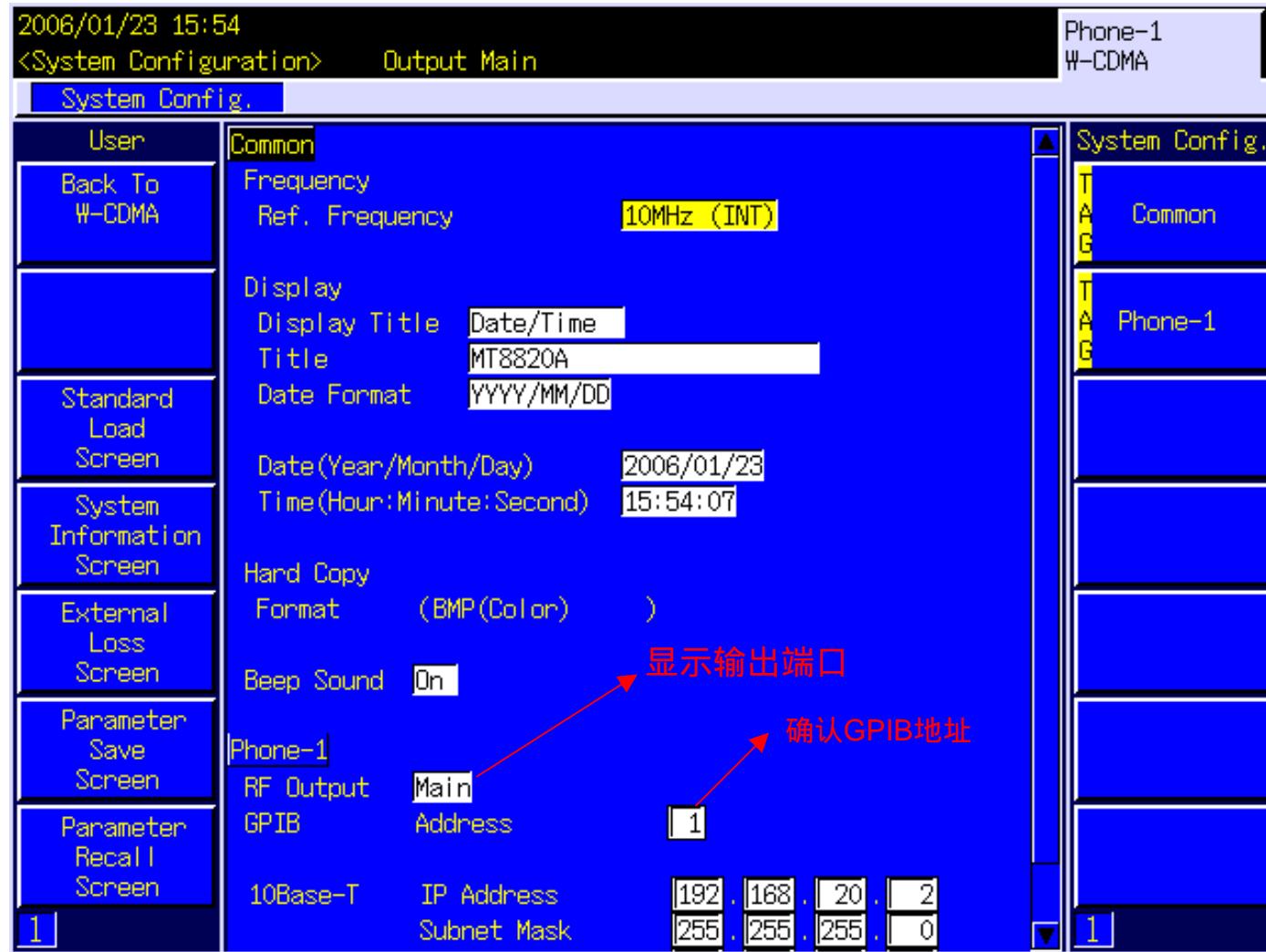
Sequence monitor : 呼叫监控

**Screen : 选择屏幕 , 同时有parameter/fundamental/PS report
或者parameter/sequence monitor/UE report**

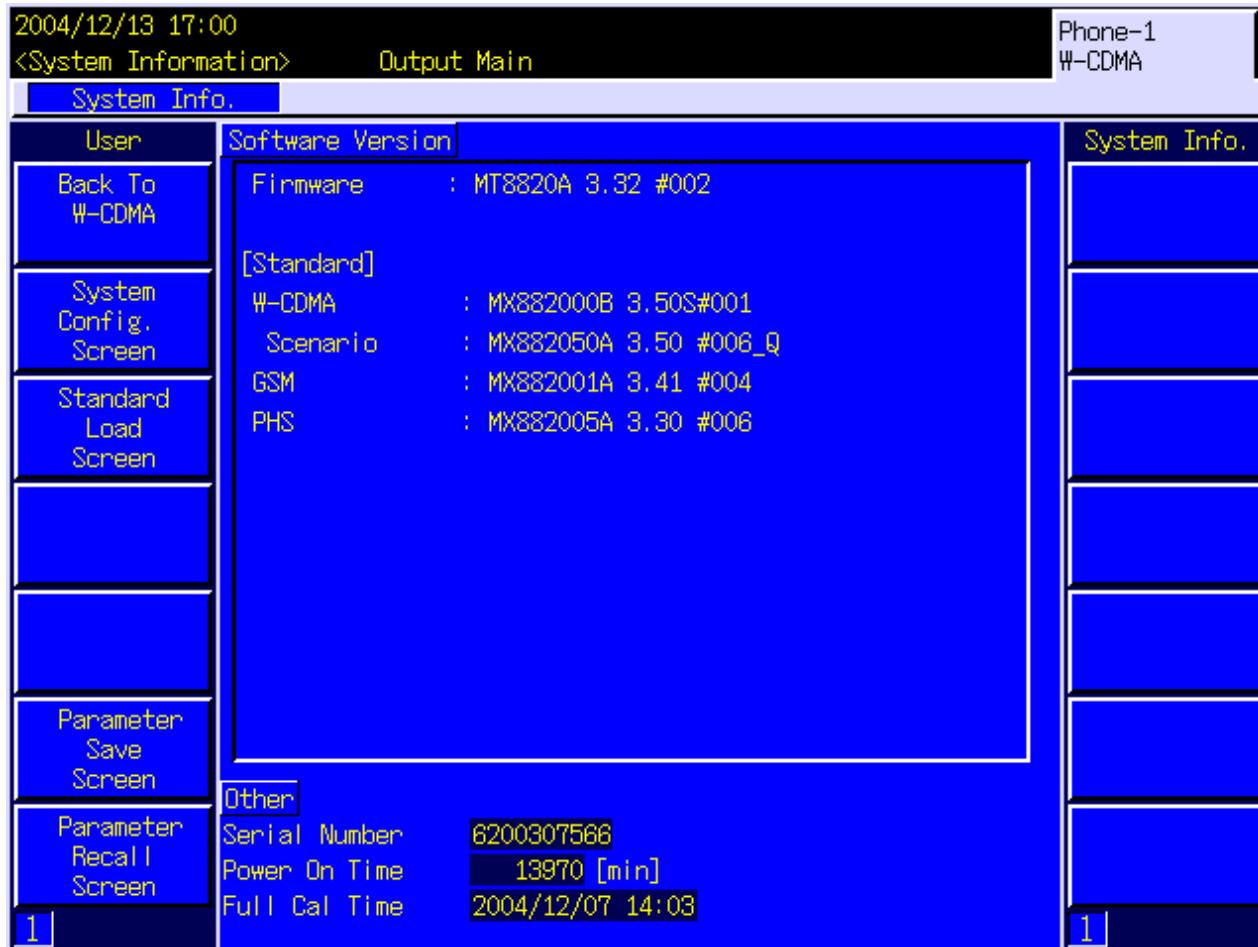
查看系统的
配置：

按Config→
System info





确认scenario 版本



升级:

- (1) 插入CF卡
- (2) 同时按set 和开机键
- (3) 听到滴声
松手，系统会自动升级

MT8820A一般参数设置

线损设置

urement.

Viewing a parameter window

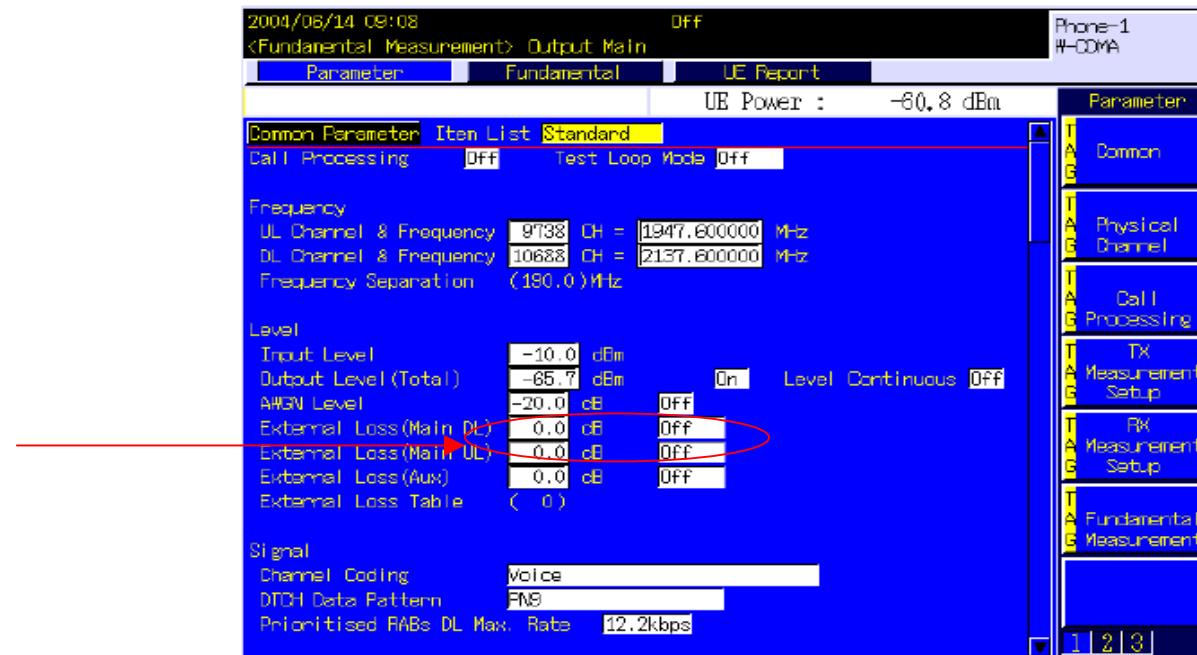


Fig. 3.2.1-1 Fundamental Measurement screen - Parameter window
(Full Window)

参数 (1)

2004/11/07 14:52 Loop Mode 1 Phone-1
 <Fundamental Measurement> Output Main W-CDMA

Parameter	Fundamental	UE Report
UE Power : -2.7 dBm		
Common Parameter Item List Standard		
Call Processing	<input checked="" type="checkbox"/> On	Test Loop Mode <input checked="" type="checkbox"/> Mode 1
Frequency		
UL Channel & Frequency	<input type="text"/> 9738	CH = <input type="text"/> 1947.600000 MHz
DL Channel & Frequency	<input type="text"/> 10688	CH = <input type="text"/> 2137.600000 MHz
Frequency Separation	(190.0) MHz	
Level		
Input Level	<input type="text"/> -15.0	dBm
Output Level (Total)	<input type="text"/> -65.7	dBm
AWGN Level	<input type="text"/> -20.0	dB
External Loss(Main DL)	<input type="text"/> -2.0	dB
External Loss(Main UL)	<input type="text"/> 2.0	dB
External Loss(Aux)	<input type="text"/> 0.0	dB
External Loss Table	(0)	
<input checked="" type="checkbox"/> On Level Continuous <input type="checkbox"/> Off		
Signal		
Channel Coding	Reference Measurement Channel	
DTCH Data Pattern	PN9	
Prioritised RABs DL Max. Rate	12.2kbps	

Parameter
T Common
T Physical Channel
T Call Processing
T TX Measurement Setup
T RX Measurement Setup
T Fundamental Measurement
1 | 2 | 3

参数 (2)

2004/11/07 14:52 Loop Mode 1 Phone-1
<Fundamental Measurement> Output Main W-CDMA

Parameter Fundamental UE Report UE Power : -2.7 dBm

Physical Channel Parameter Item List Standard

Downlink Physical Channel	Total Power/Ior (0.0)dB
CPICH	Power(CPICH_Ec/Ior)
P-CCPCH	Power(P-CCPCH_Ec/Ior)
SCH	Power(SCH_Ec/Ior)
PICH	Power(PICH_Ec/Ior)
DPCH	Power(DPCH_Ec/Ior)
DCNS	Power(OCNS_Ec/Ior) (-60.0)dB (On)
S-CCPCH	Power(S-CCPCH_Ec/Ior)
AICH	Power(AICH_Ec/Ior)

Call Processing Parameter Item List Standard

Base Station ID

Location Area Identification

MCC	001
MNC	01
LAC	0080 H

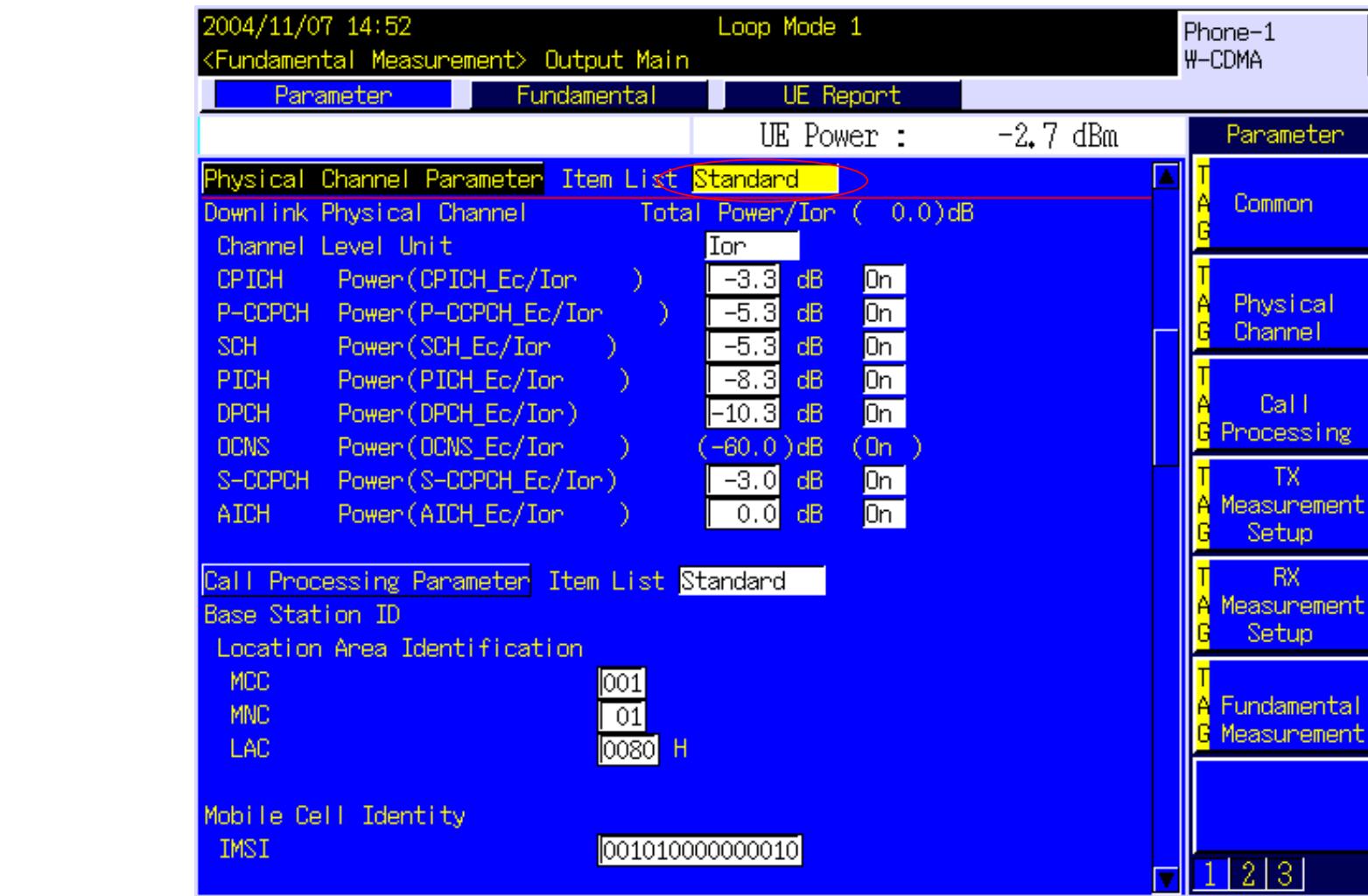
Mobile Cell Identity

IMSI

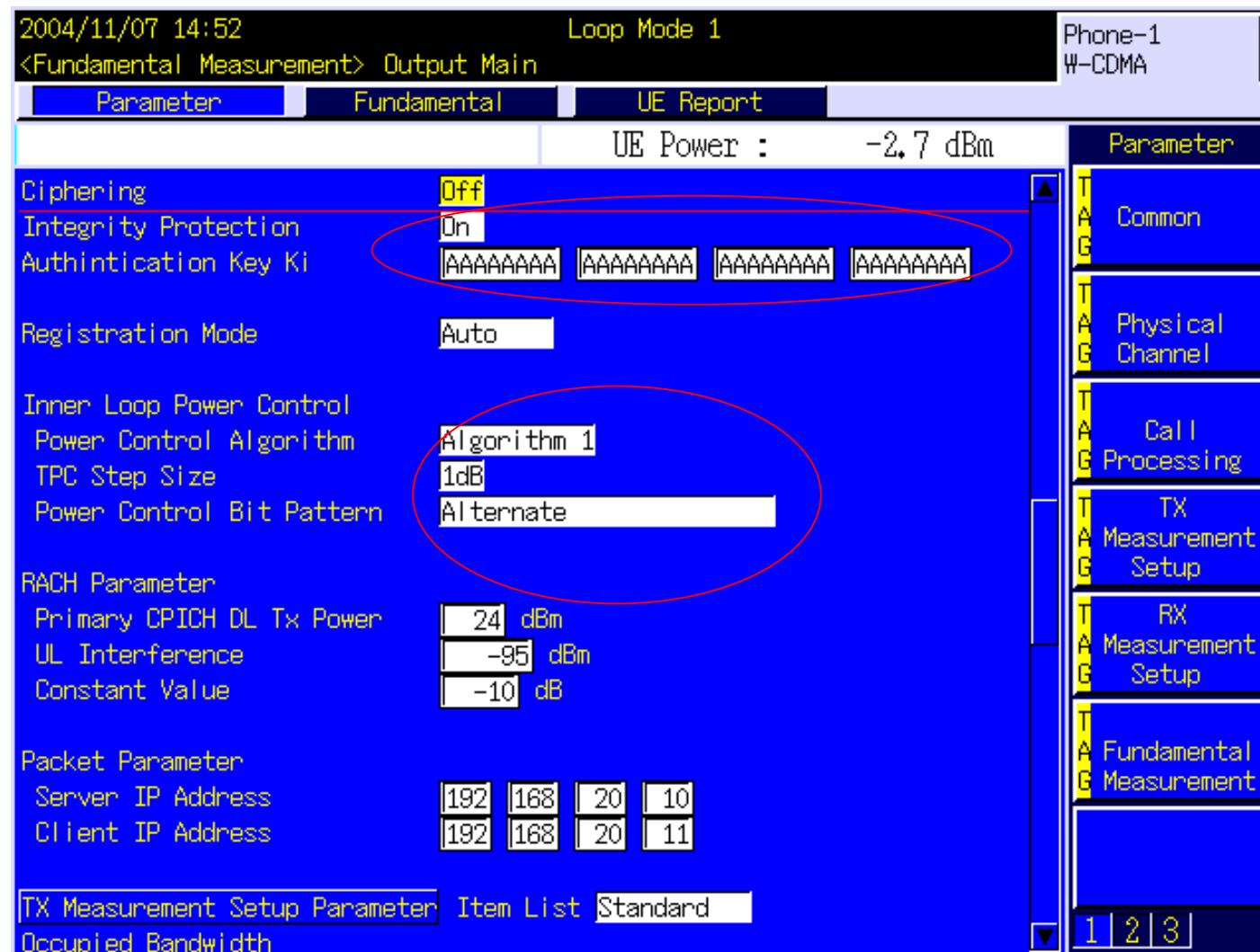
Parameter

- T A G Common
- T A G Physical Channel
- T A G Call Processing
- T A G TX Measurement Setup
- T A G RX Measurement Setup
- T A G Fundamental Measurement

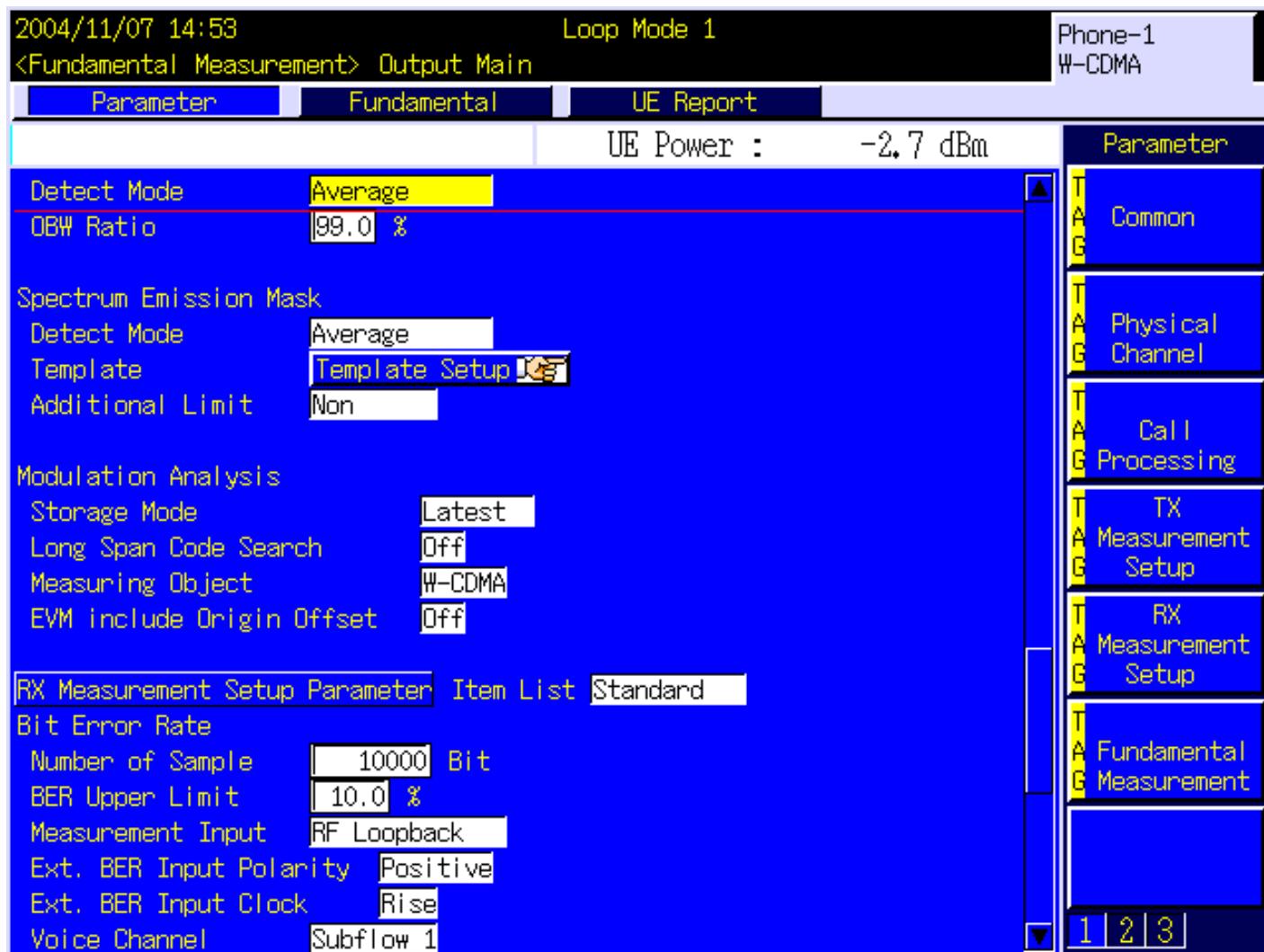
1 | 2 | 3



参数 (3)



参数 (4)



参数 (5)

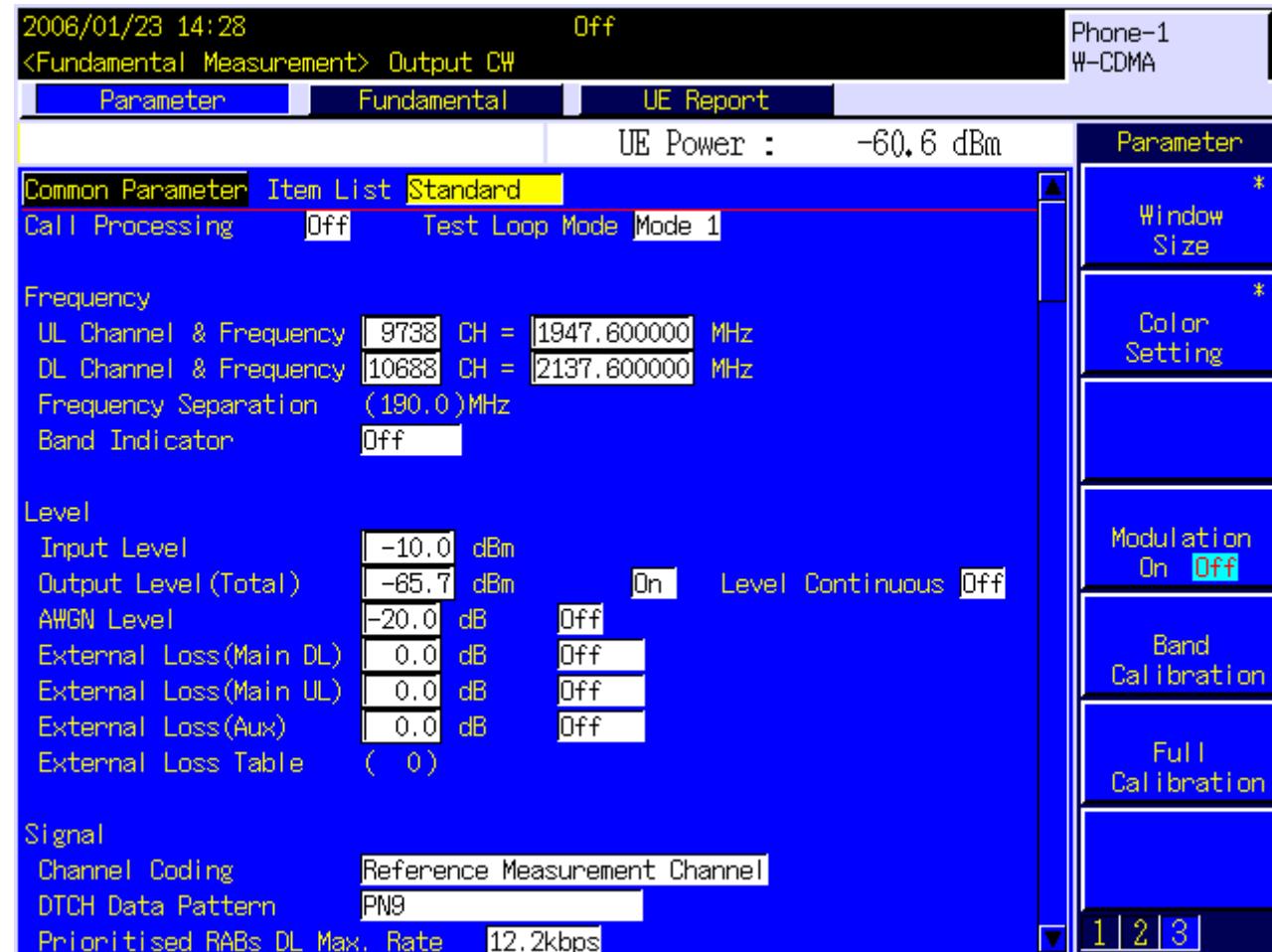
2004/11/07 14:53 Loop Mode 1 Phone-1
 <Fundamental Measurement> Output Main W-CDMA

Parameter	Fundamental	UE Report
		UE Power : -2.7 dBm
Ext. BER Input Polarity	Positive	
Ext. BER Input Clock	Rise	
Voice Channel	Subflow 1	
Block Error Rate		
Number of Sample	50 Block	
BLER Upper Limit	10.0 %	
BER/BLER Timeout Length	10 sec	
Fundamental Measurement Parameter Item List Standard		
Measurement Mode	Fast	
Power Measurement	On	Average Count 20
Frequency Error	On	Average Count 20
Occupied Bandwidth	On	Average Count 20
Spectrum Emission Mask	On	Average Count 20
Adjacent Channel Power	On	Average Count 20
Modulation Analysis	On	Average Count 20
Peak Code Domain Error	On	Average Count 20
BER	On	
BLER	On	

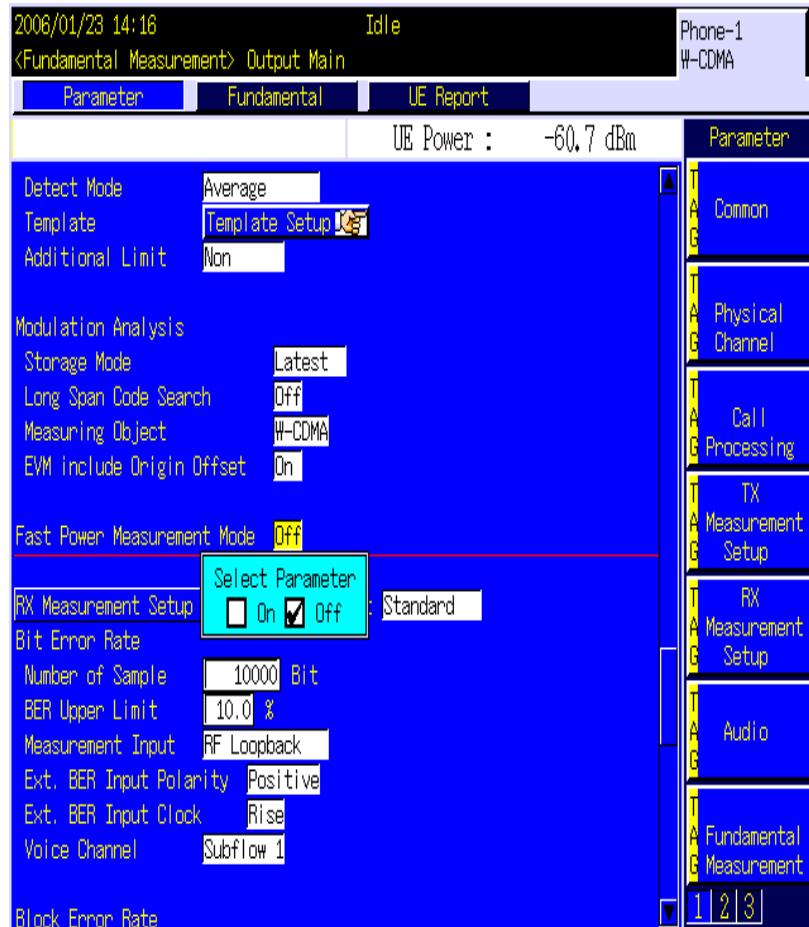
Parameter
 T A G Common
 T A G Physical Channel
 T A G Call Processing
 T A G TX Measurement Setup
 T A G RX Measurement Setup
 T A G Fundamental Measurement
 1 2 3

MT8820A相关参数设置说明

输出CW信号:



Fast Power Measurement Mode :



当 Fast Power Measurement Mode 被设成 On , 可以加快测量功率的速度 ;
但同时 , 其它的TX项目不可以被测量。

Basic Spectrum parameter Setting :



MT8820A WCDMA测试步骤

WCDMA 测试项目

- 1, 最大输出功率<TX>**
- 2, 频率误差 <TX>**
- 3, 占用带宽 <TX>**
- 4, 频谱发射模板 <TX>**
- 5, 邻信道功率泄漏比 <TX>**
- 6, 矢量幅度误差 <TX>**
- 7, 参考灵敏度<RX>**
- 8, 最小输出功率 <TX>**
- 9, 内环功率控制 <TX>**
- 10, 最大输入电平<RX>**

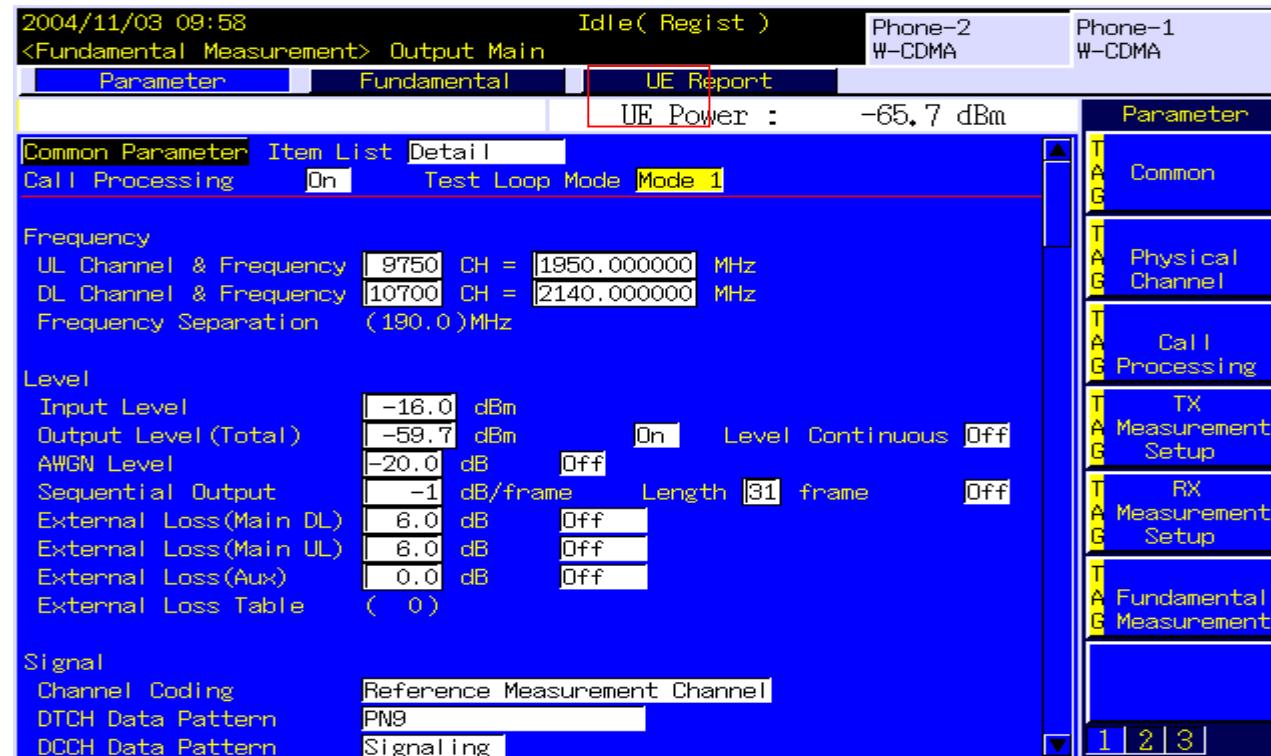
最大输出功率

push

Focus

Parameter

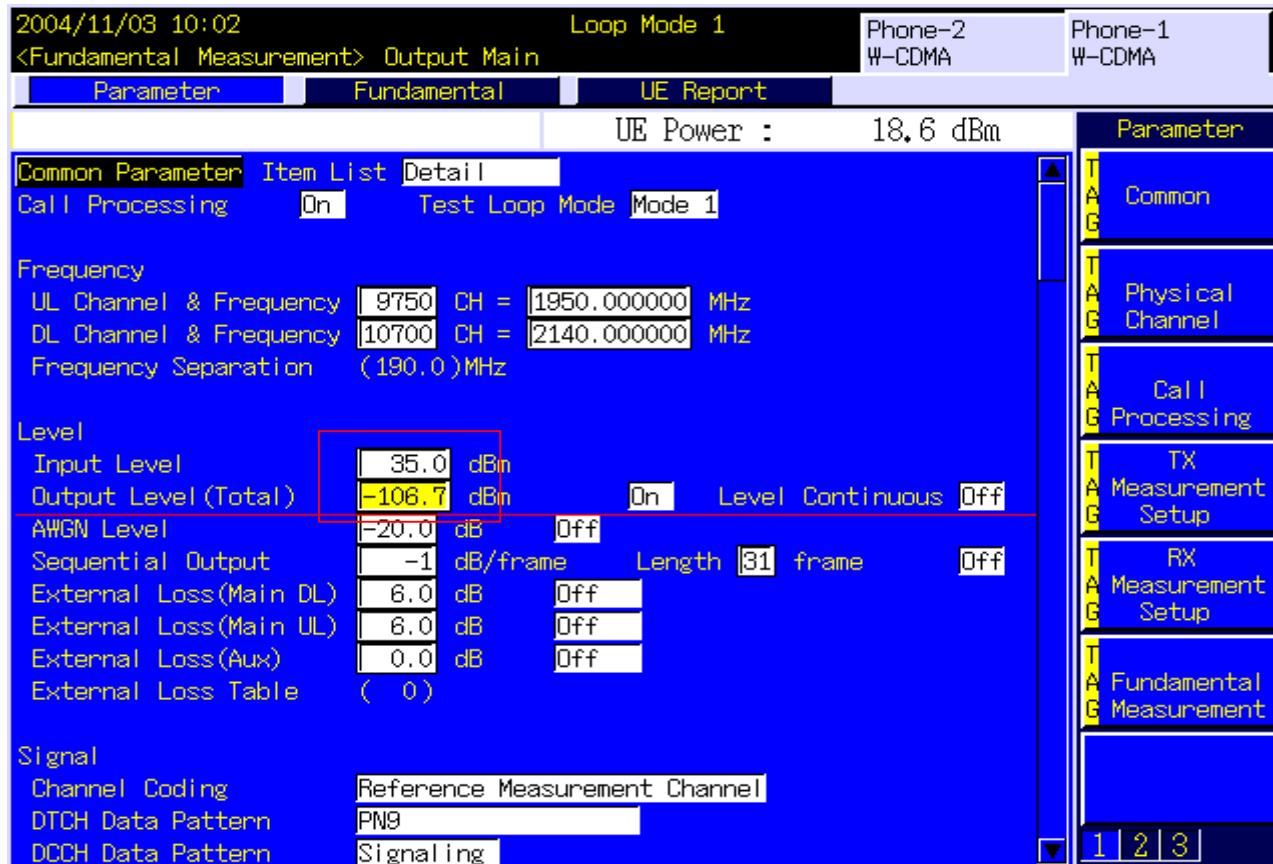
1. Connect to Test Loop Mode1.



最大输出功率

2 set Input Level to +35.0dBm.

3 set Output Level to -106.7dBm.



最大输出功率

4 set TPC Pattern to ALL1.

Parameter		Fundamental	UE Report	Loop Mode 1	Phone-2 W-CDMA	Phone-1 W-CDMA
				UE Power :		18.5 dBm
Measurements Report	Off					
Ciphering	Off					
Integrity Protection	On					
Call Drop Threshold	256	Frame	Off			
Inner Loop Power Control						
Power Control Algorithm	Algorithm 1					
TPC Step Size	1dB					
Power Control Bit Pattern	All 1					
User Command	10101 01010 10101 01010 10101 01010					
Compressed Mode Pattern	Off					
GSM DL Signal	Set1					
GSM DL Signal	Off					
Neighbour Cell Allocation						
Intra - Primary Scrambling Code	Off Off Off Off Off Off Off Off					
Inter - DL UARFCN	Off Off Off Off Off Off Off Off					

最大输出功率

5. set Power Measurement to On.

6 set the average count of power measurement to 20 times.

The screenshot shows a software interface for fundamental measurement setup. At the top, it displays the date and time (2004/11/03 10:05), loop mode (Loop Mode 1), and two phone configurations (Phone-2 W-CDMA and Phone-1 W-CDMA). Below this, there are tabs for Parameter, Fundamental, and UE Report, with the Fundamental tab selected. The main area shows various parameters: Ext. BER Input Polarity (Positive), Ext. BER Input Clock (Rise), Voice Channel (Subflow 1), Block Error Rate, Number of Sample (50 Block), BLER Upper Limit (10.0 %), and BER/BLER Timeout Length (10 sec). On the right, a vertical stack of measurement categories is listed: Common, Physical Channel, Call Processing, TX Measurement Setup, RX Measurement Setup, and Fundamental Measurement. A red arrow points to the 'Power Measurement' row under the 'Fundamental Measurement Parameter' section, highlighting the 'Average Count' field which is set to 20.

Measurement	Status	Average Count
Power Measurement	On	20
Frequency Error	On	20
Occupied Bandwidth	On	20
Spectrum Emission Mask	On	20
Adjacent Channel Power	On	20
Modulation Analysis	On	20
Peak Code Domain Error	On	20
BER	Off	
BLER	Off	

最大输出功率

push

Focus

Single / continuous

7 perform power measurement.

8 read the result of power measurement.

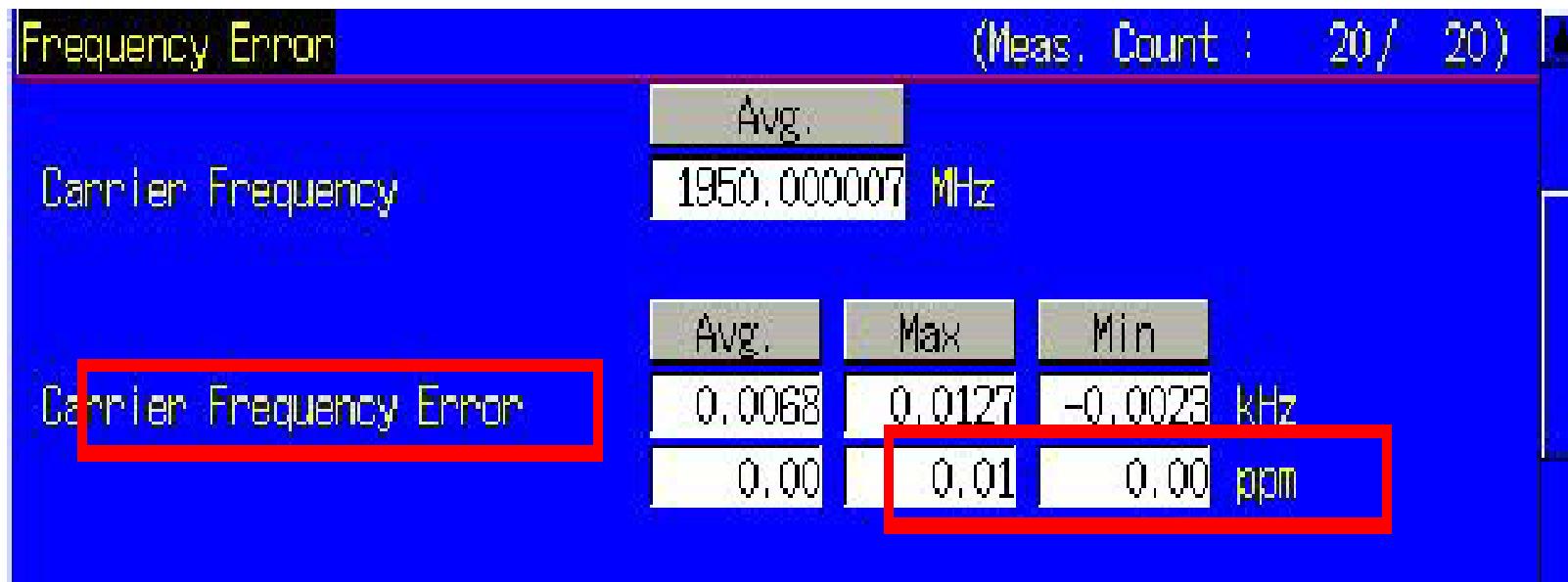
Power Measurement				(Meas. Count : 20 / 20)
	Avg.	Max	Min	
TX Power	23.16	23.18	23.14	dBm
	207.0	207.9	206.1	mW
Filtered Power	22.94	22.97	22.91	dBm
	197.0	198.3	195.6	mW

TX Power corresponds to Mean power (5MHz band), and Filtered Power corresponds to RRC filtered mean power.

频率误差

- 1 Test Loop Mode1.
- 2 set Input Level to +35.0dBm.
- 3 set Output Level to -106.7dBm.
- 4 set TPC Pattern to ALL1.
- 5 set Frequency Measurement to On.
- 6 set the average count of frequency measurement to 20 times.
- 7 perform frequency measurement.
- 8 read the result of frequency error measurement.

频率误差



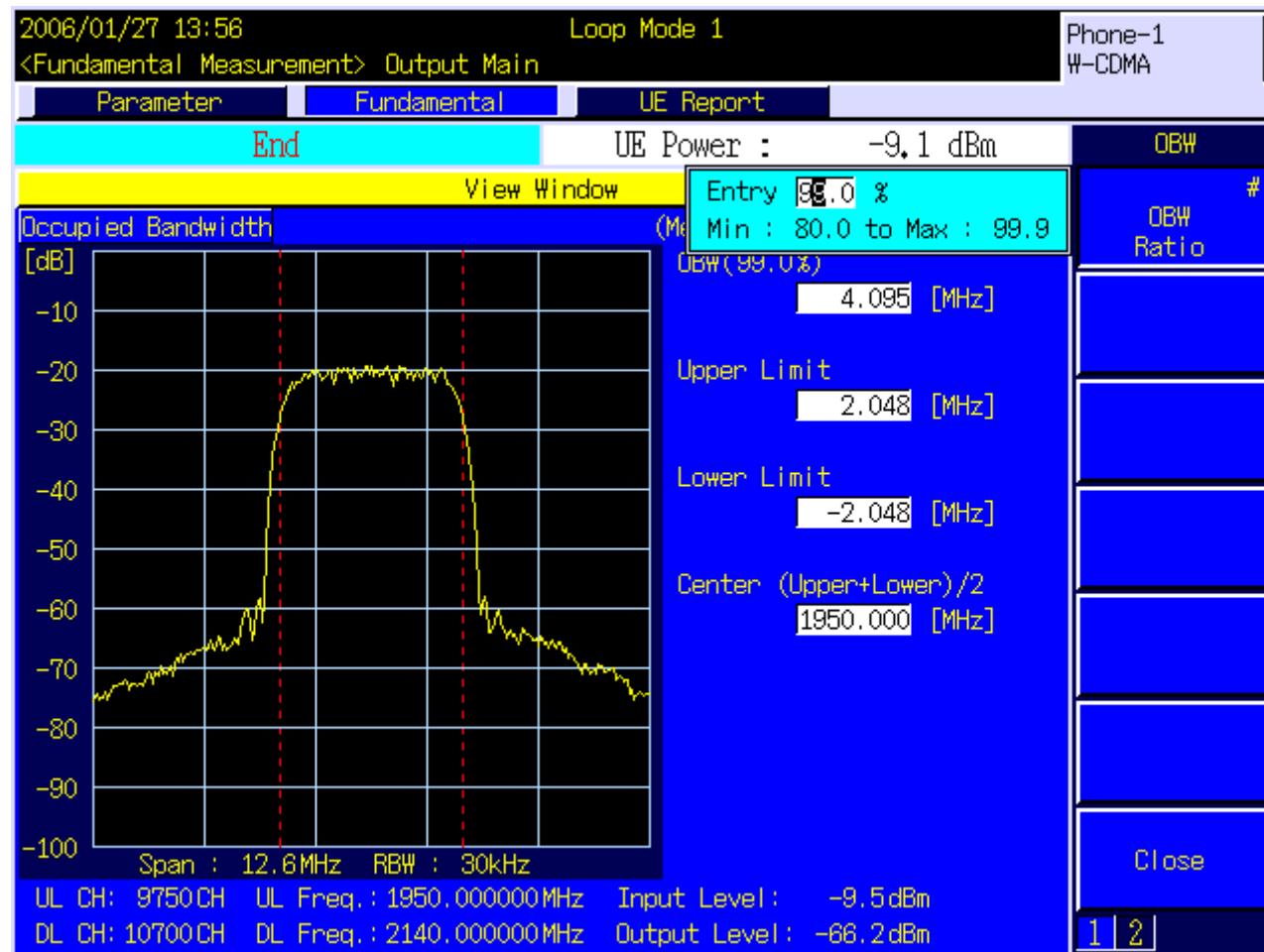
占用带宽

- 1 Test Loop Mode1.
- 2 set Input Level to +35.0dBm.
- 3 set Output Level to -106.7dBm.
- 4 set TPC Pattern to ALL1.
- 5 set OBW Measurement to On.
- 6 set the average count of OBW measurement to 20 times.
- 7 perform measurement.
- 8 read the result

占用带宽

Occupied Bandwidth	View	(Meas. Count : 20 / 20)
OB4	4.118 MHz	
Upper Frequency	2.059 MHz	
Lower Frequency	-2.059 MHz	
Center (Upper+Lower)/2	1950.000 MHz	

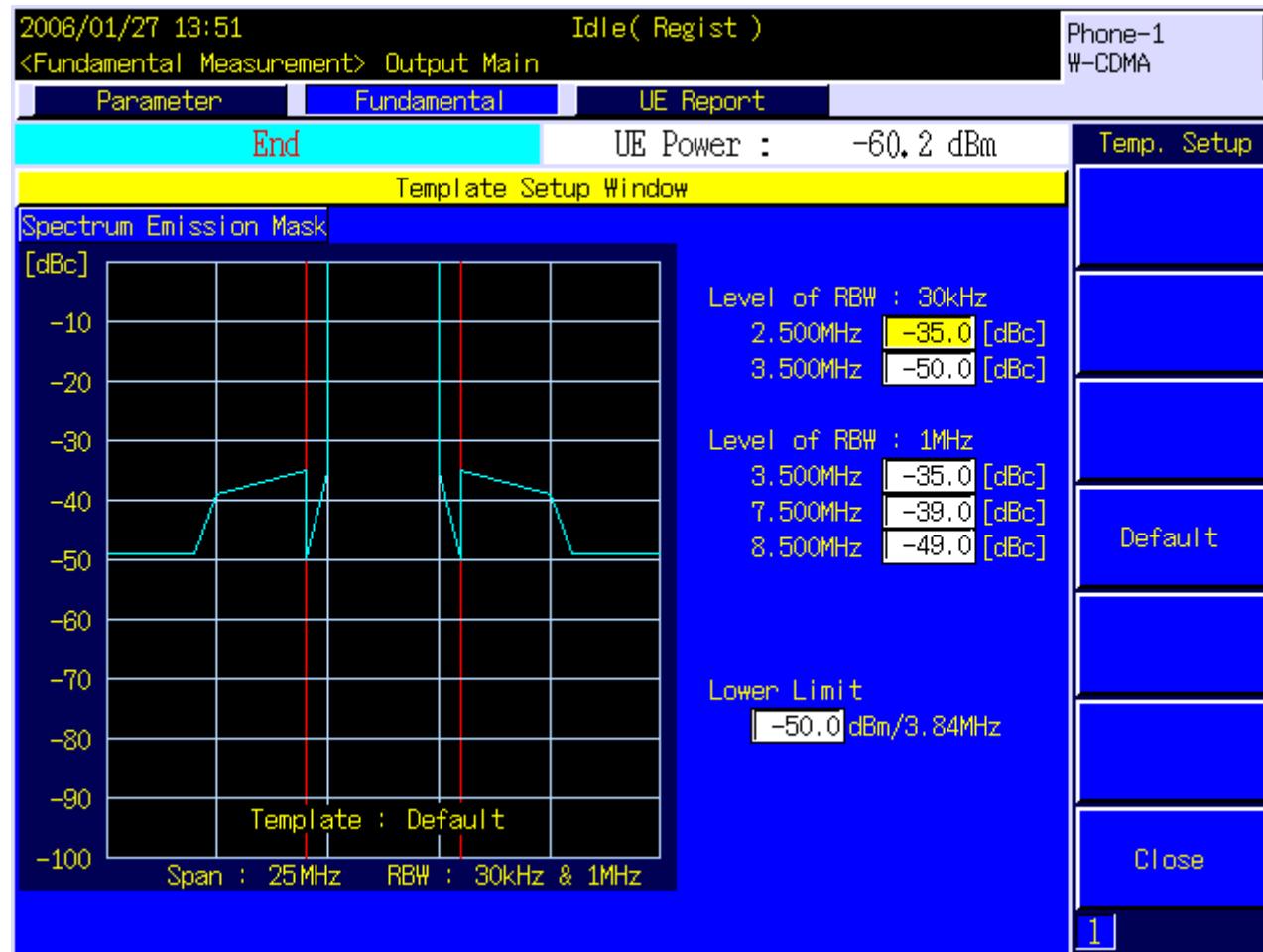
占用带宽-图形



频谱发射模板

- 1 Test Loop Mode1.
- 2 set Input Level to +35.0dBm.
- 3 set Output Level to -106.7dBm.
- 4 set TPC Pattern to ALL1.
- 5 set SEM Measurement to On.
- 6 set the average count of SEM measurement to 20 times.
- 7 perform measurement.
- 8 read the result .

频谱发射模板一图形

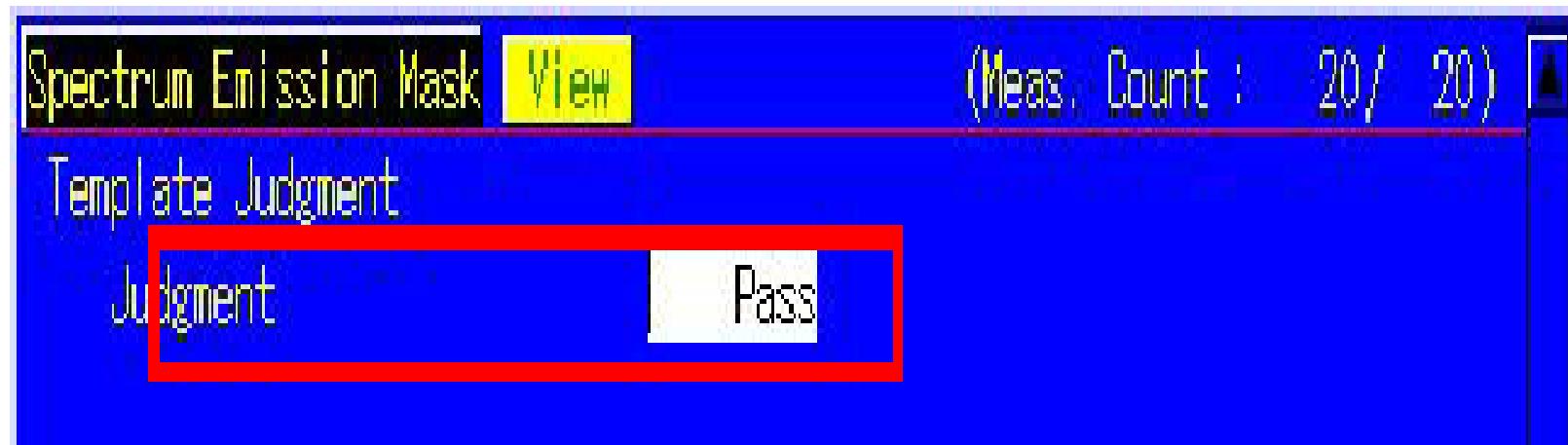


频谱发射模板一图形

Measured value shown in yellow, template shown in blue



频谱发射模板



邻信道功率泄漏比

- 1 Test Loop Mode1.
- 2 set Input Level to +35.0dBm.
- 3 set Output Level to -106.7dBm.
- 4 set TPC Pattern to ALL1.
- 5 set ACLR Measurement to On.
- 6 set the average count of ACLR measurement to 20 times.
- 7 perform Measurement .
- 8 read the result.

邻信道功率泄漏比

Adjacent Channel Power				(Meas. Count : 20 / 20)
Leakage power due to Modulation				
Offset Freq.	Power	Avg.	Max	Min
-10 MHz	-46.20	-46.00	-46.40	dB
-5 MHz	-37.31	-36.99	-37.68	dB
5 MHz	-36.50	-36.21	-36.80	dB
10 MHz	-48.25	-48.06	-48.38	dB

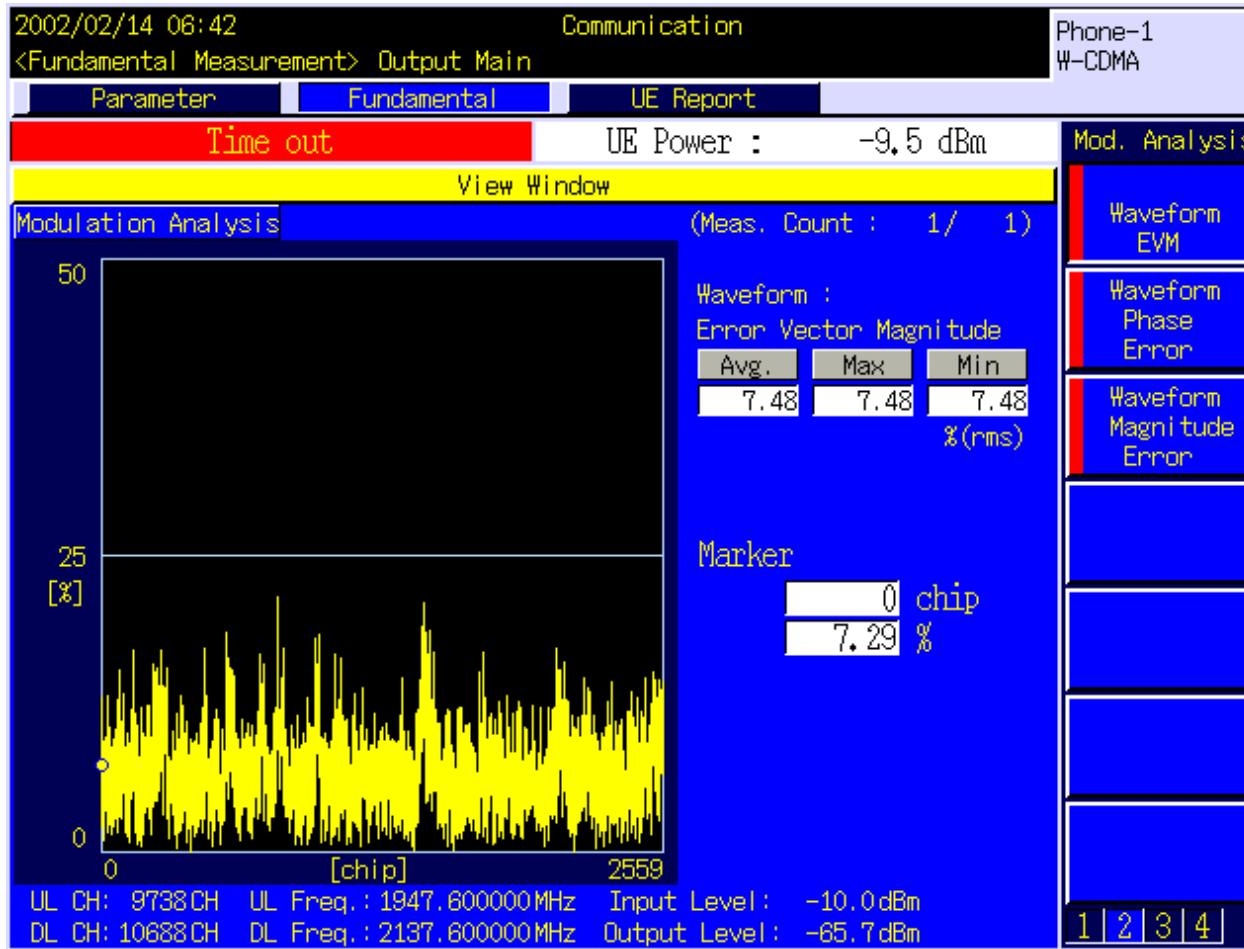
矢量幅度误差 (EVM)

- 1 Test Loop Mode1.
- 2 set Input Level to +35.0dBm.
- 3 set Output Level to -106.7dBm.
- 4 set TPC Pattern to ALL1.
- 5 set EVM Measurement to On.
- 6 set the average count of EVM measurement to 20 times.
- 7 perform measurement.
- 8 read the result.

矢量幅度误差 (EVM)

Modulation Analysis		View			(Meas. Count : 20 / 20)	
		Avg	Max	Min		
Error Vector Magnitude		7.31	7.44	7.13	% (rms)	
Peak Vector Error		17.34	18.51	15.87	%	
Phase Error		3.14	3.23	3.02	deg. (rms)	
Magnitude Error		4.83	4.88	4.76	% (rms)	
Origin Offset		-25.78	-25.52	-26.10	dB	
IQ Imbalance		102.75	103.51	101.99	% (I/Q)	
Timing Error		0.4	0.5	0.3	chip	
DPCOCH/DPDCH Power Ratio		-5.48	-5.44	-5.51	dB	

调制精度—图形

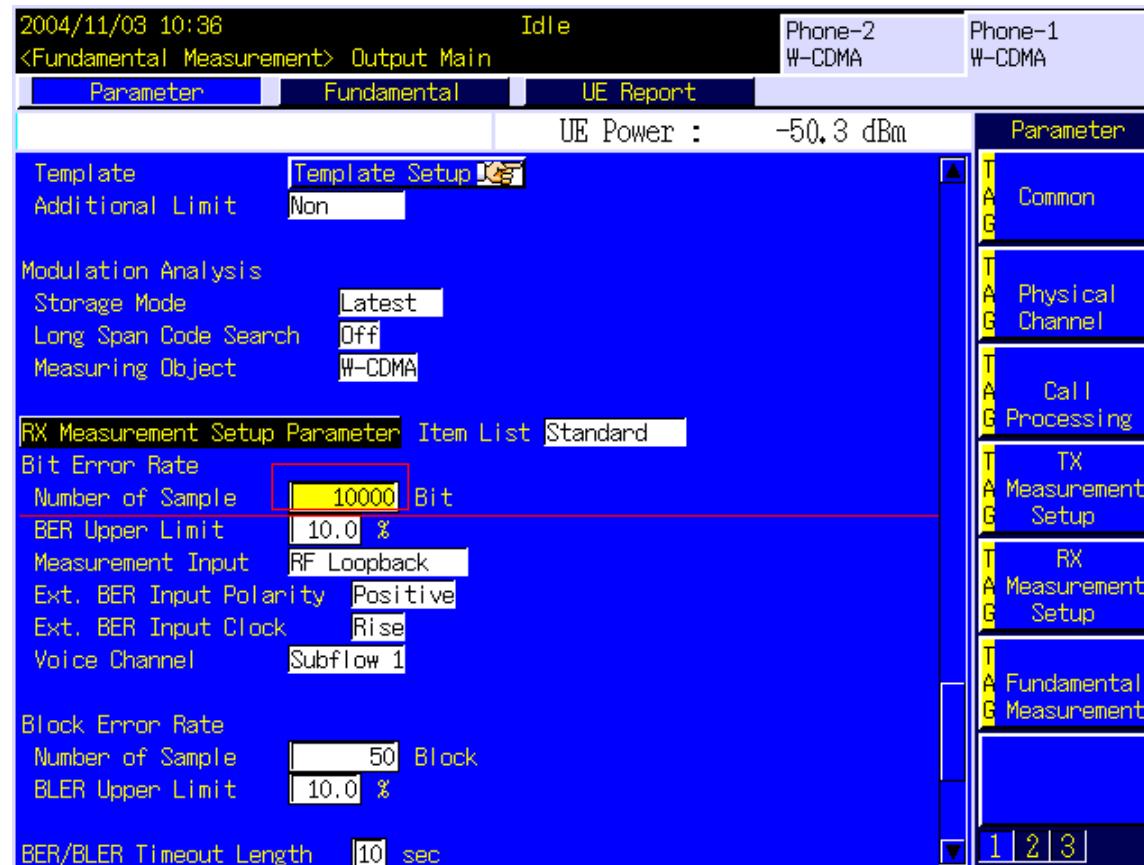


- Select modulation type to view
- EVM
- Phase
- Magnitude

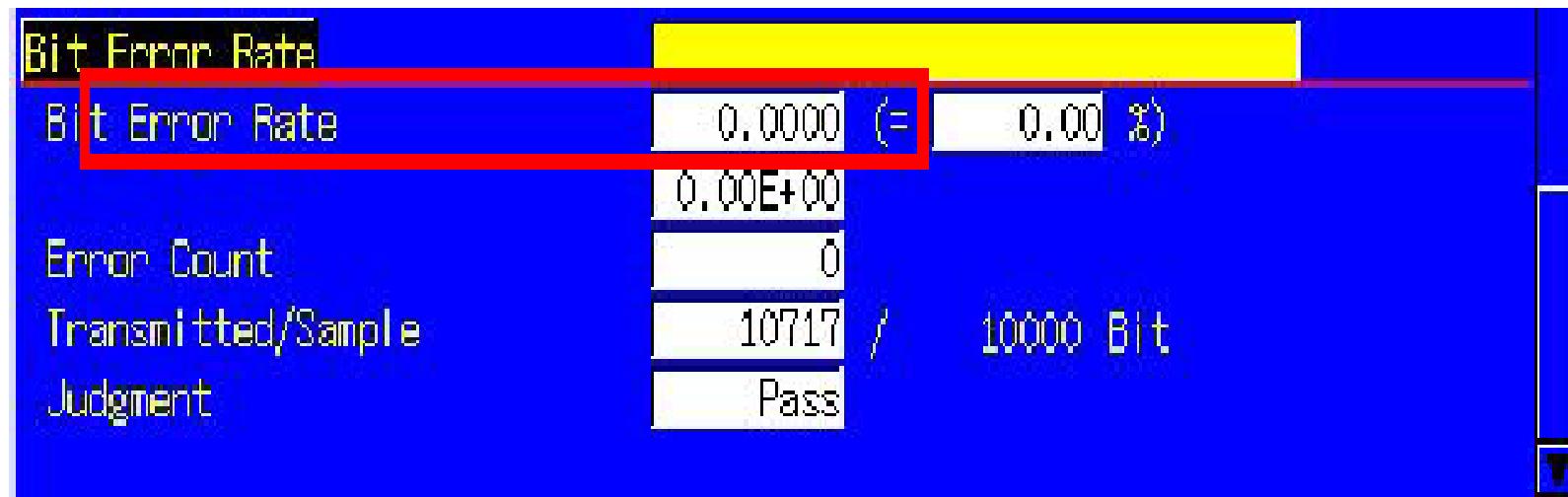
参考灵敏度测试

- 1 Test Loop Mode1.
- 2 set Input Level to +35.0dBm.
- 3 set Output Level to -106.7dBm.
- 4 set TPC Pattern to ALL1.
- 5 set BER Measurement to On.
- 6 set the number of BER measurement samples to 10000 bits.
- 7 perform measurement.
- 8 read the result.

参考灵敏度测试



参考灵敏度测试



最小输出功率测试

- 1.Test Loop Mode1.
- 2.set Input Level to -20.0dBm.
- 3.set Output Level to -93dBm.
- 4.set TPC Pattern to ALL0.
- 5.set Power Measurement to On.
- 6.set the average count of power measurement to 20 times.
- 7.perform the measurement.
- 8.read the result of power measurement.

最小输出功率测试

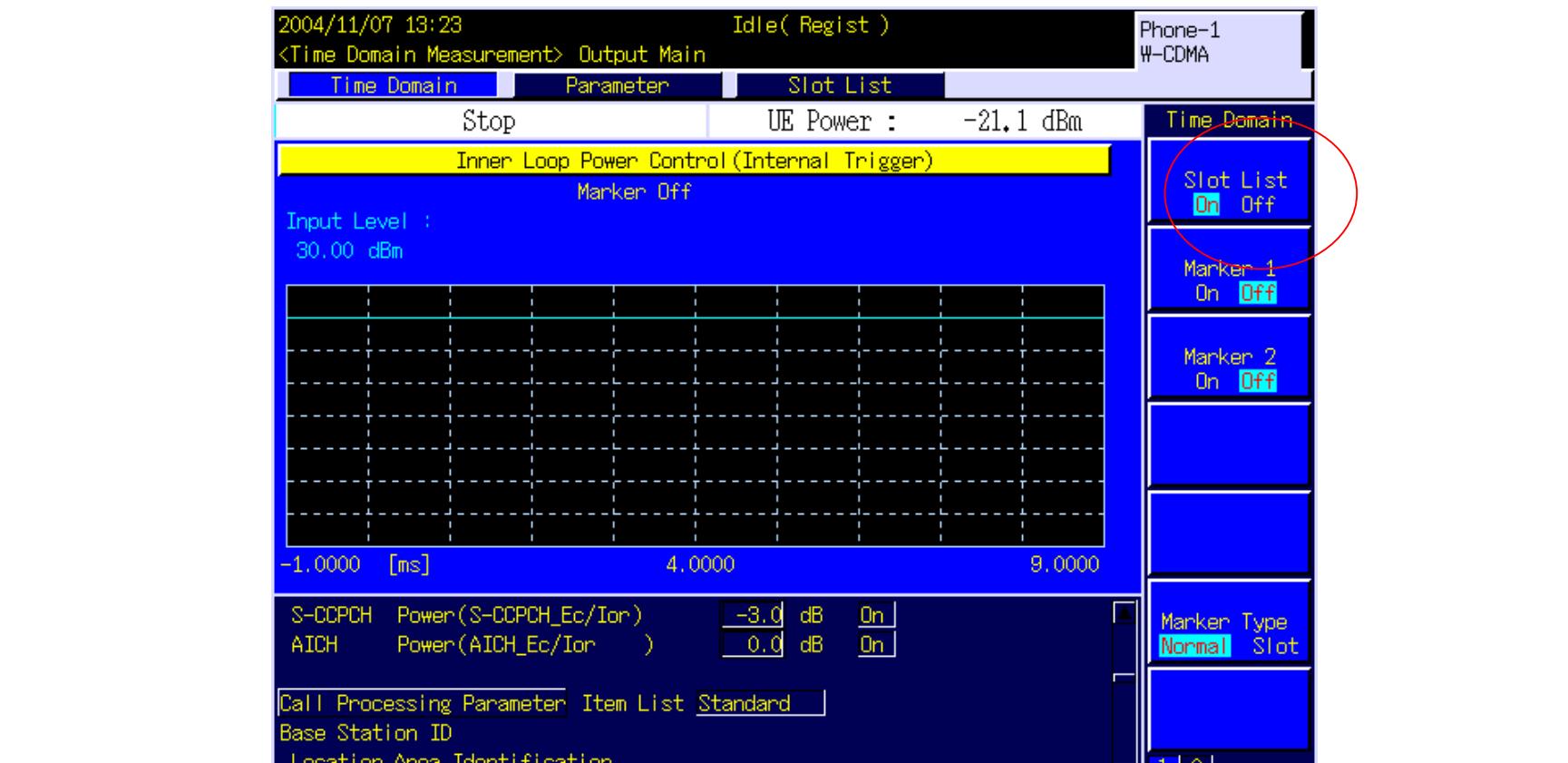
Power Measurement			(Meas. Count : 20 / 20)	
	Avg.	Max	Min	
Tx Power	-57.02	-56.93	-57.10	dBm
	1.985	2.025	1.950	nW
Filtered Power	-58.92	-58.78	-58.05	dBm
	1.282	1.325	1.244	nW

内环功率控制测试

1. display the Time Domain Measurement screen.
2. set Measurement Object to Inner Loop Power Control.
3. display a slot list.
4. register Slot0~Slot59 for the slot list.
5. set Time Span of Time Domain measurement to 40.0ms.
6. set TPC Algorithm to 2.
7. set TPC Step Size to 1dB.
8. set RRC Filter to Off.
9. Connect to Test Loop Mode1.

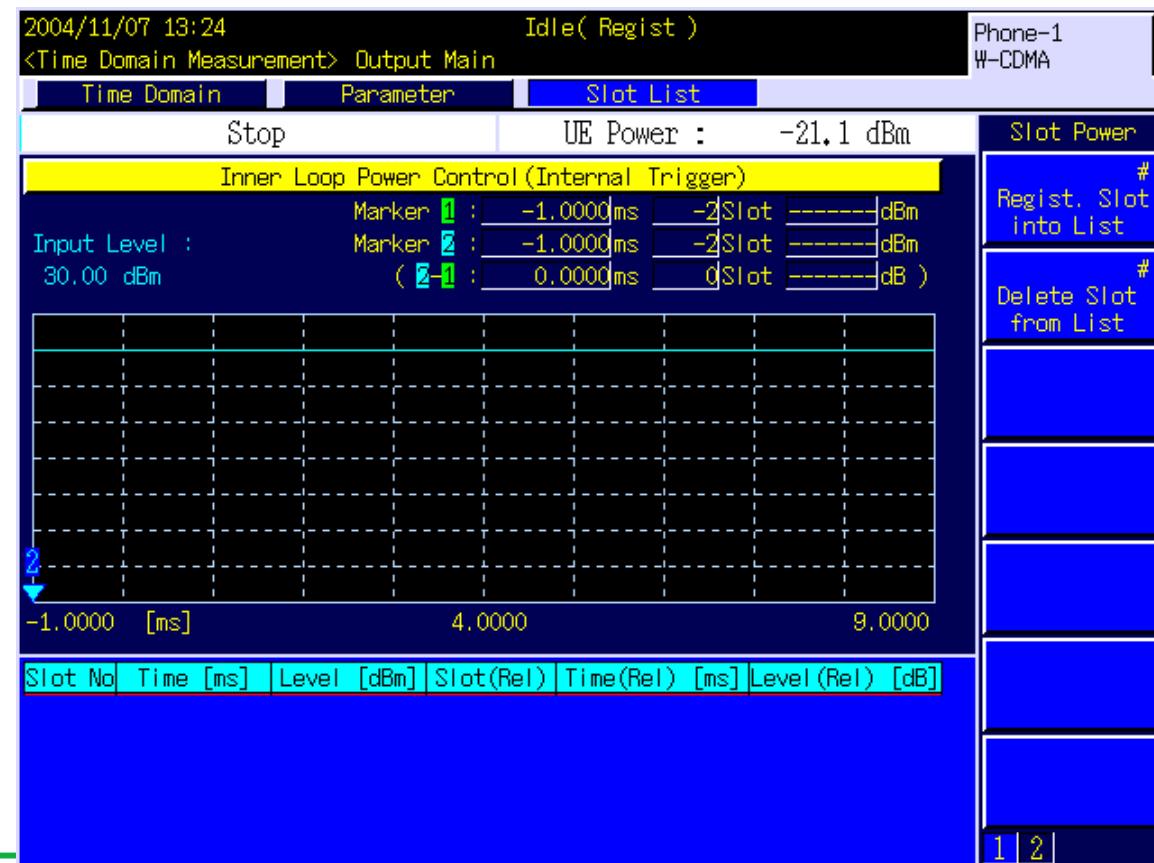
内环功率控制测试

display a slot list.



内环功率控制测试

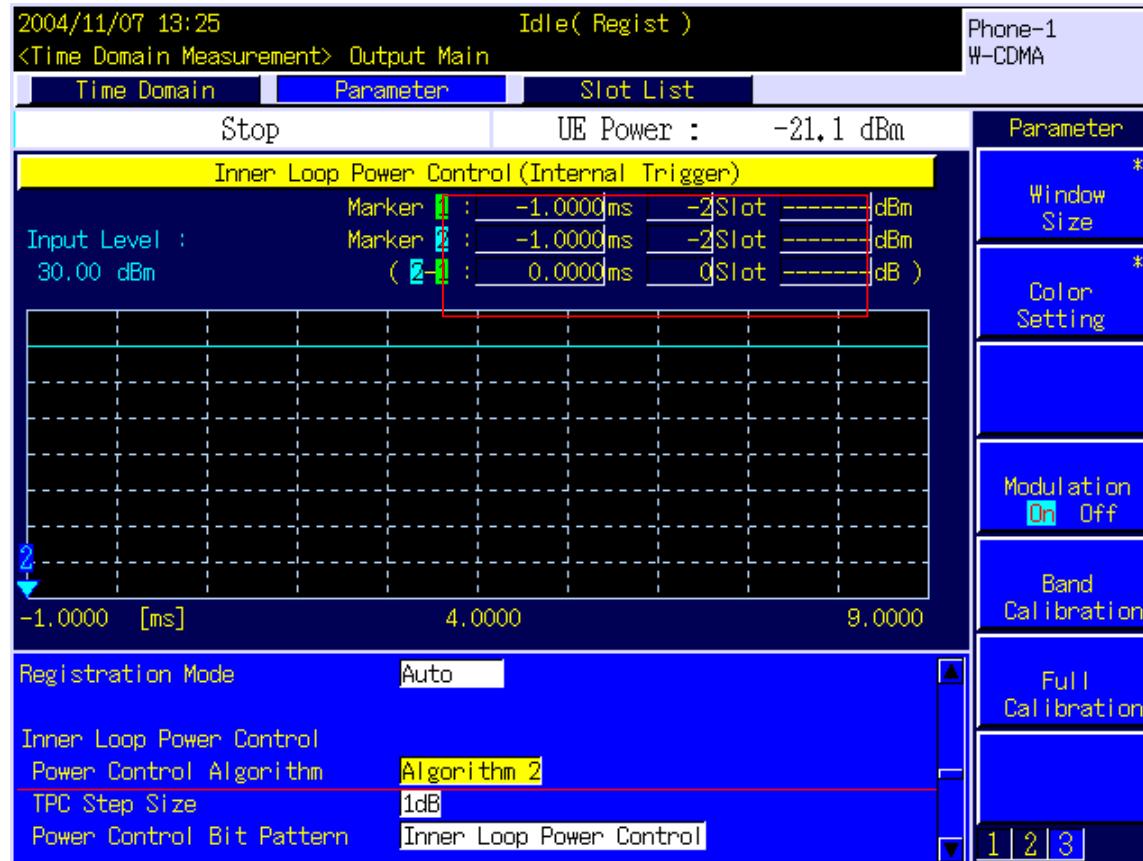
register Slot0~Slot59 for the slot list.



内环功率控制测试

set TPC Algorithm to 2.

set TPC Step Size to 1dB.



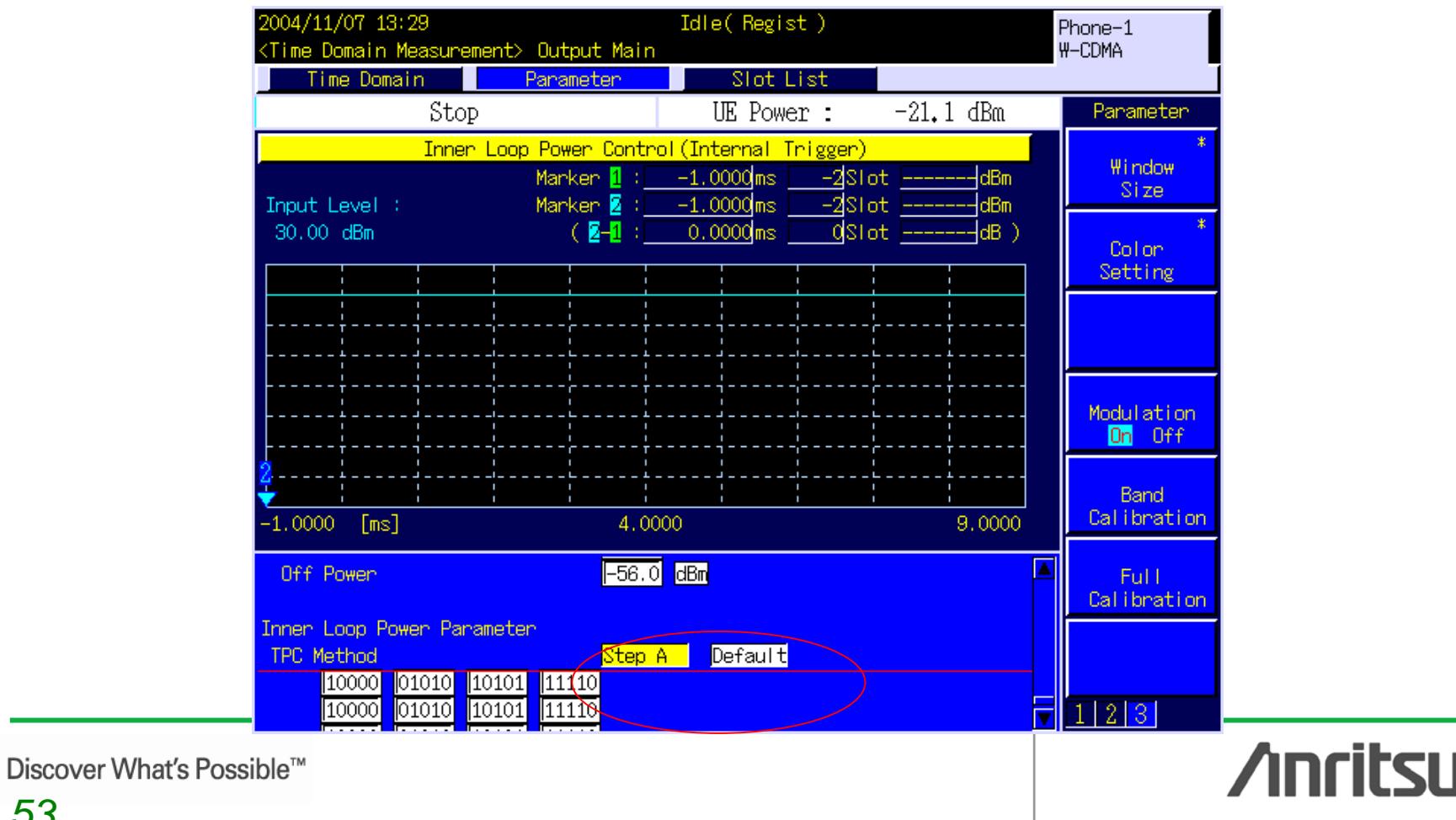
内环功率控制测试 (stepA)

1. set TPC Test Step to A.
2. set TPC Algorithm to 2.
3. set TPC Step Size to 1dB.
4. set TPC Pattern to Inner Loop Power Control.
- 5-1 set Output Level to -65.7dBm.
5. set Input Level to -10.0dBm.
6. set TPC Pattern to Alternate.
7. set Input Level to 0.0dBm.
8. perform the measurement.
9. read the measurement result.

** In some cases, several dBs lower/higher value from Input Level is required for UE output power before starting Inner Loop Power Control measurement.

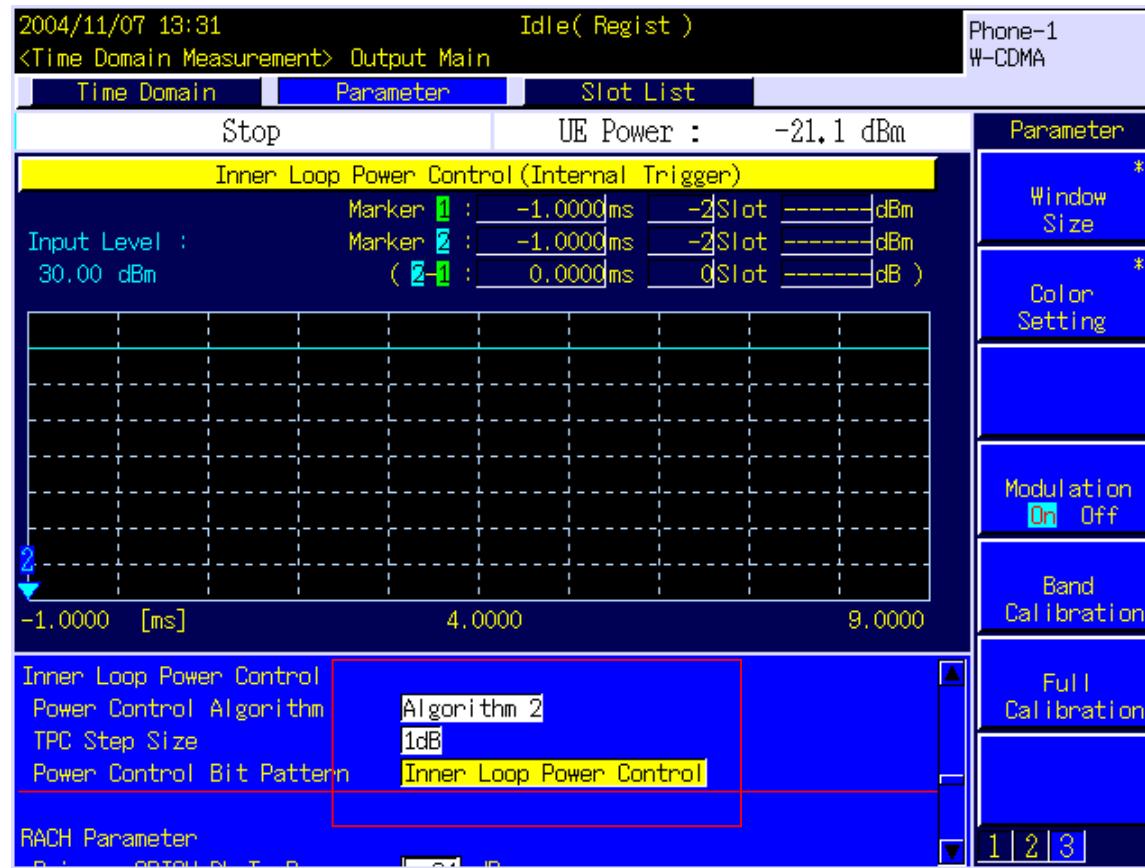
内环功率控制测试 (stepA)

set TPC Test Step to A

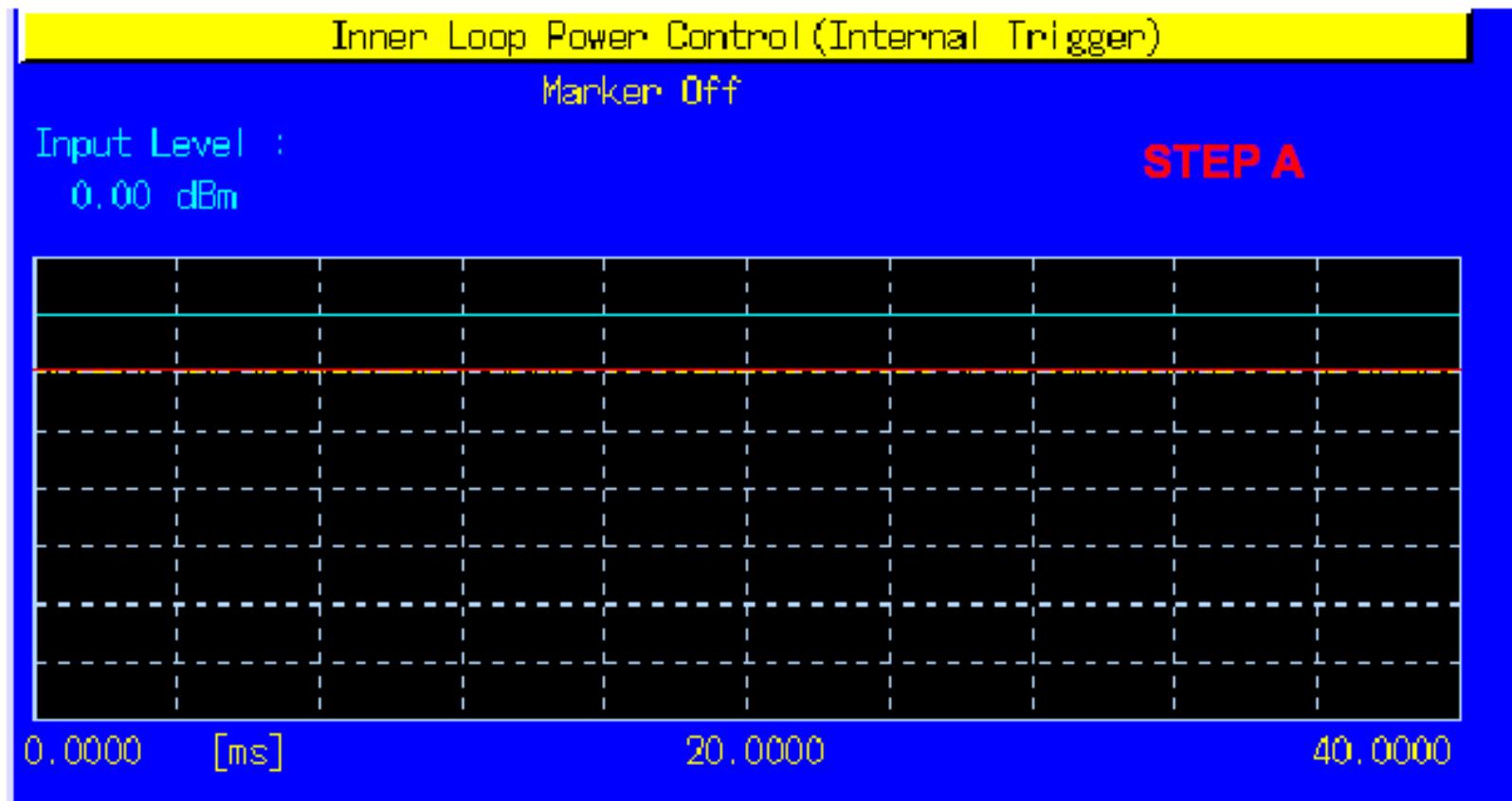


内环功率控制测试 (stepA)

set TPC Pattern to Inner Loop Power Control.



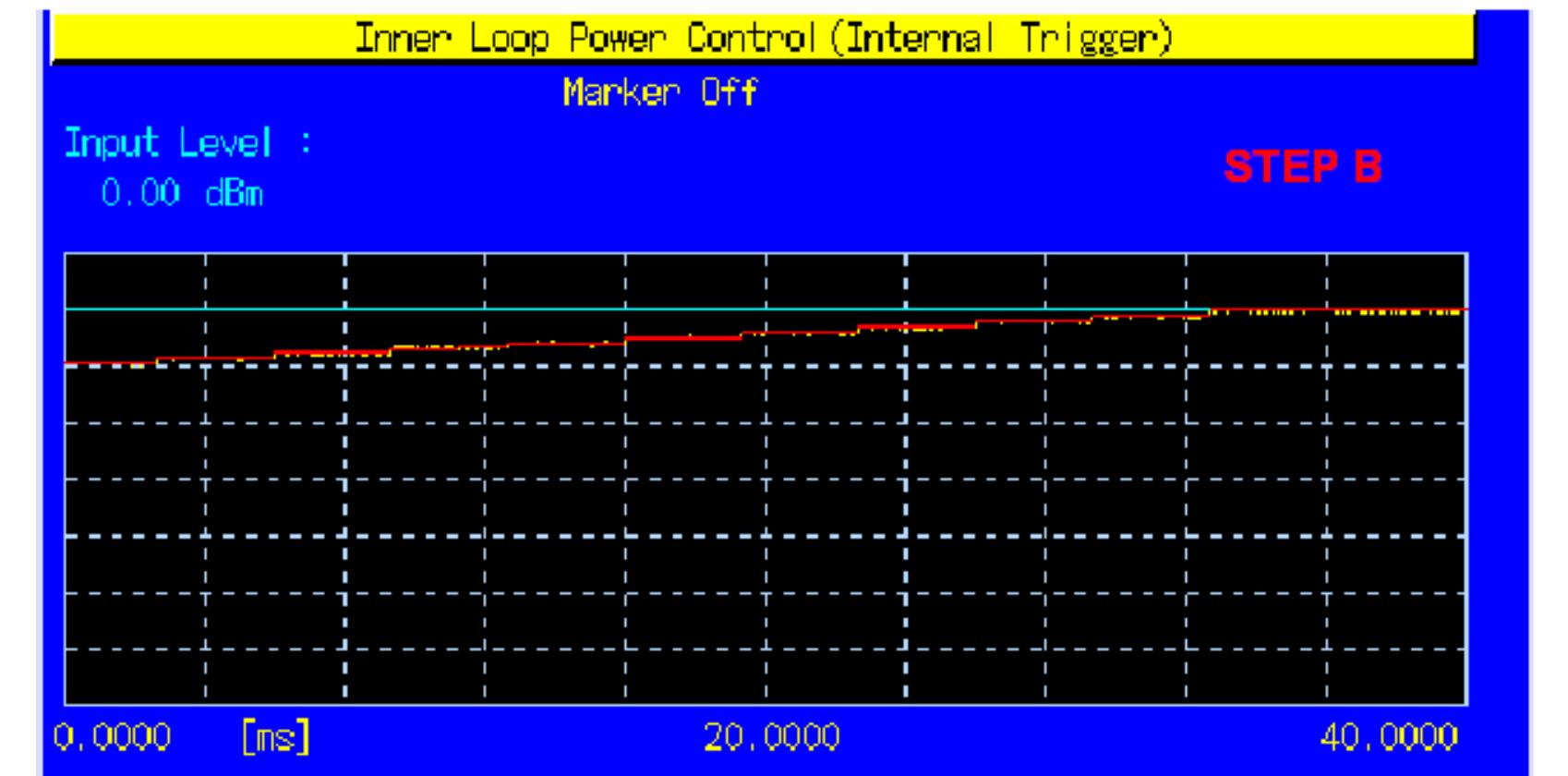
内环功率控制测试 (stepA)



内环功率控制测试 (stepB)

10. set TPC Test Step to B.
11. set TPC Algorithm to 2.
12. set TPC Step Size to 1dB.
13. set TPC Pattern to Alternate.
14. set Input Level to 0.0dBm.
15. perform the measurement.
16. read the measurement result.

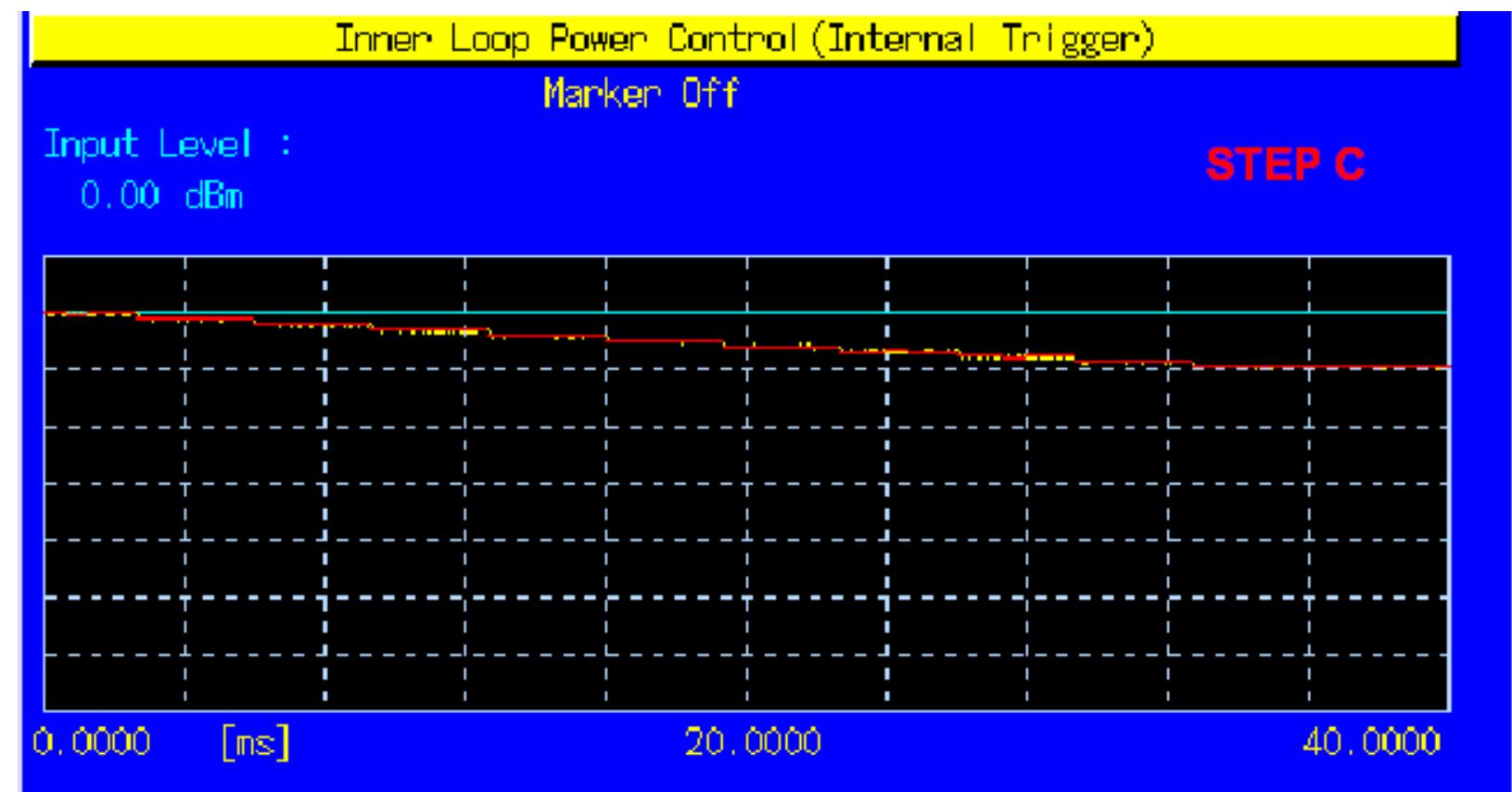
内环功率控制测试 (stepB)



内环功率控制测试 (stepC)

17. set TPC Test Step to C.
18. set TPC Algorithm to 2.
19. set TPC Step Size to 1dB.
20. set TPC Pattern to Alternate.
21. set Input Level to 0.0dBm.
22. perform the measurement.
23. read the measurement result.

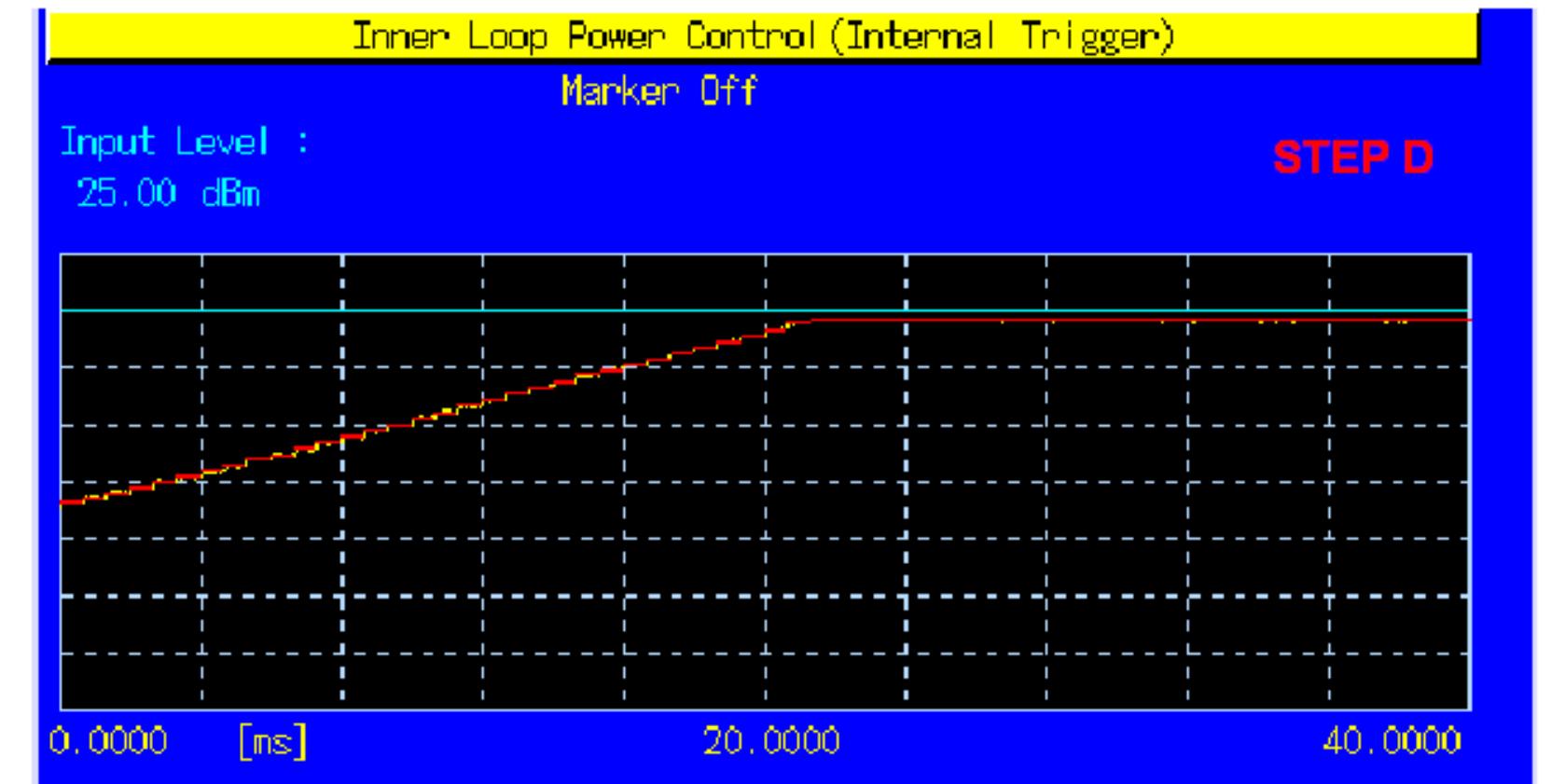
内环功率控制测试 (stepC)



内环功率控制测试 (stepD)

24. set TPC Test Step to D.
25. set TPC Algorithm to 1.
26. set TPC Step Size to 1dB.
27. set TPC Pattern to Alternate.
28. set Input Level to +25.0dBm.
29. perform the measurement.
30. read the measurement result.

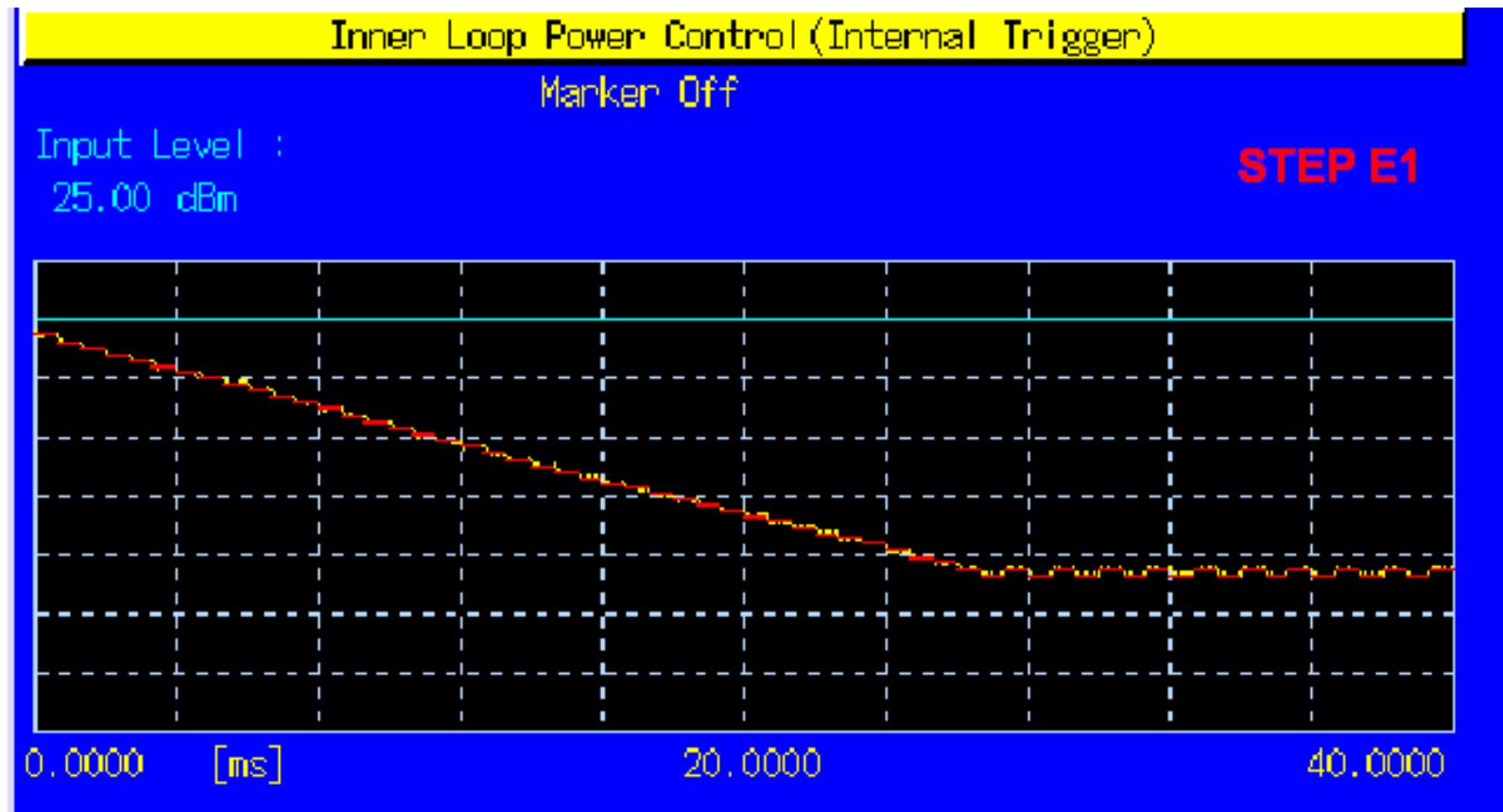
内环功率控制测试 (stepD)



内环功率控制测试(stepE1)

31. set TPC Test Step to E.
32. set the number of slots in Test Step E to 40.
33. set TPC Algorithm to 1.
34. set TPC Step Size to 1dB.
35. set TPC Pattern to Alternate.
36. set Input Level to +25.0dBm.
37. perform the measurement.
38. read the measurement result.

内环功率控制测试(stepE1)

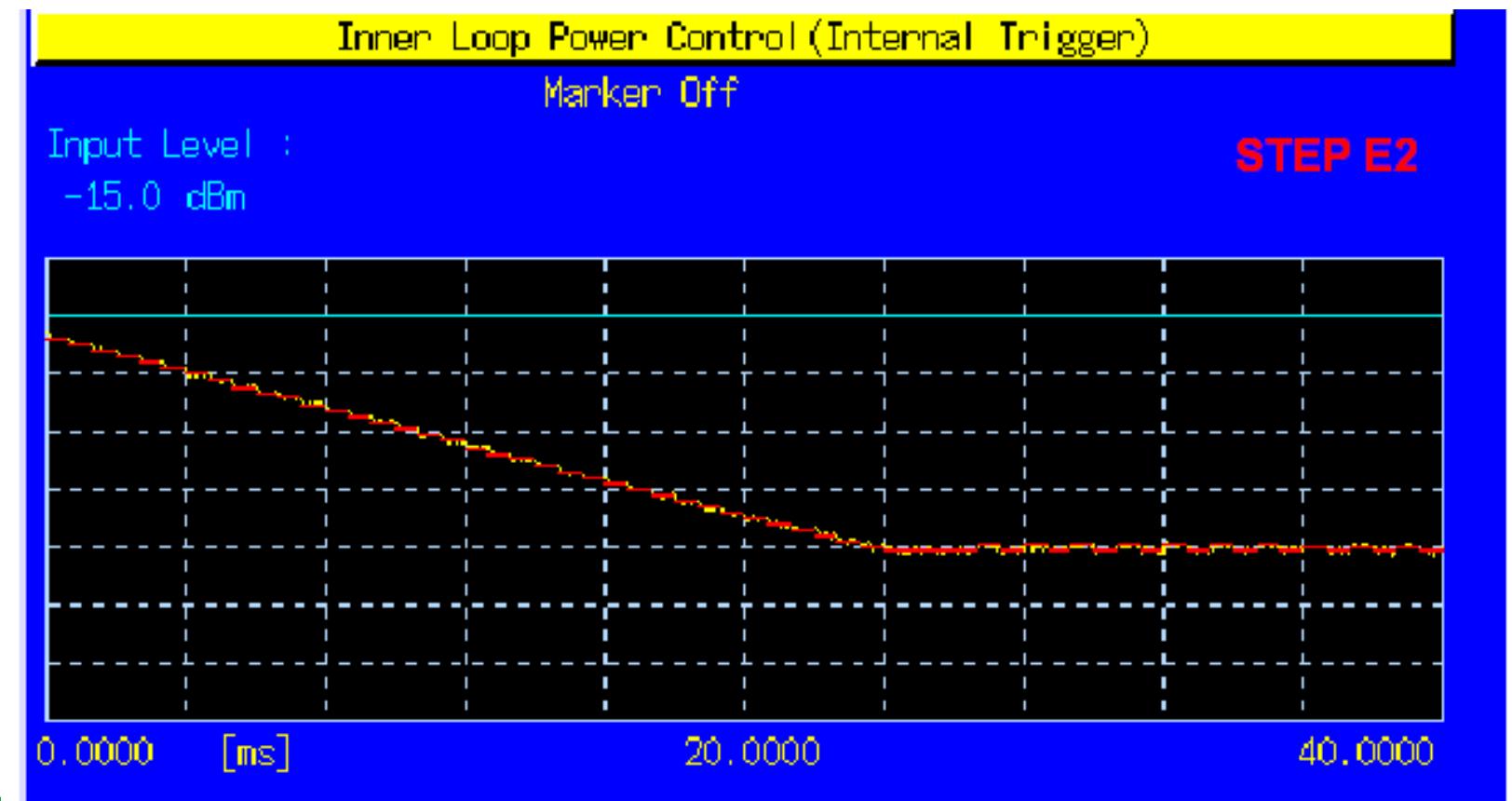


MT8820A's dynamic range (40dB) , segment the power control sequence into smaller subsequence

内环功率控制测试(stepE2)

39. set TPC Test Step to E.
40. set the number of slots in Test Step E to 40.
41. set TPC Algorithm to 1.
42. set TPC Step Size to 1dB.
43. set TPC Pattern to Alternate.
44. set Input Level to -15.0dBm.
45. perform the measurement.
46. read the measurement result.

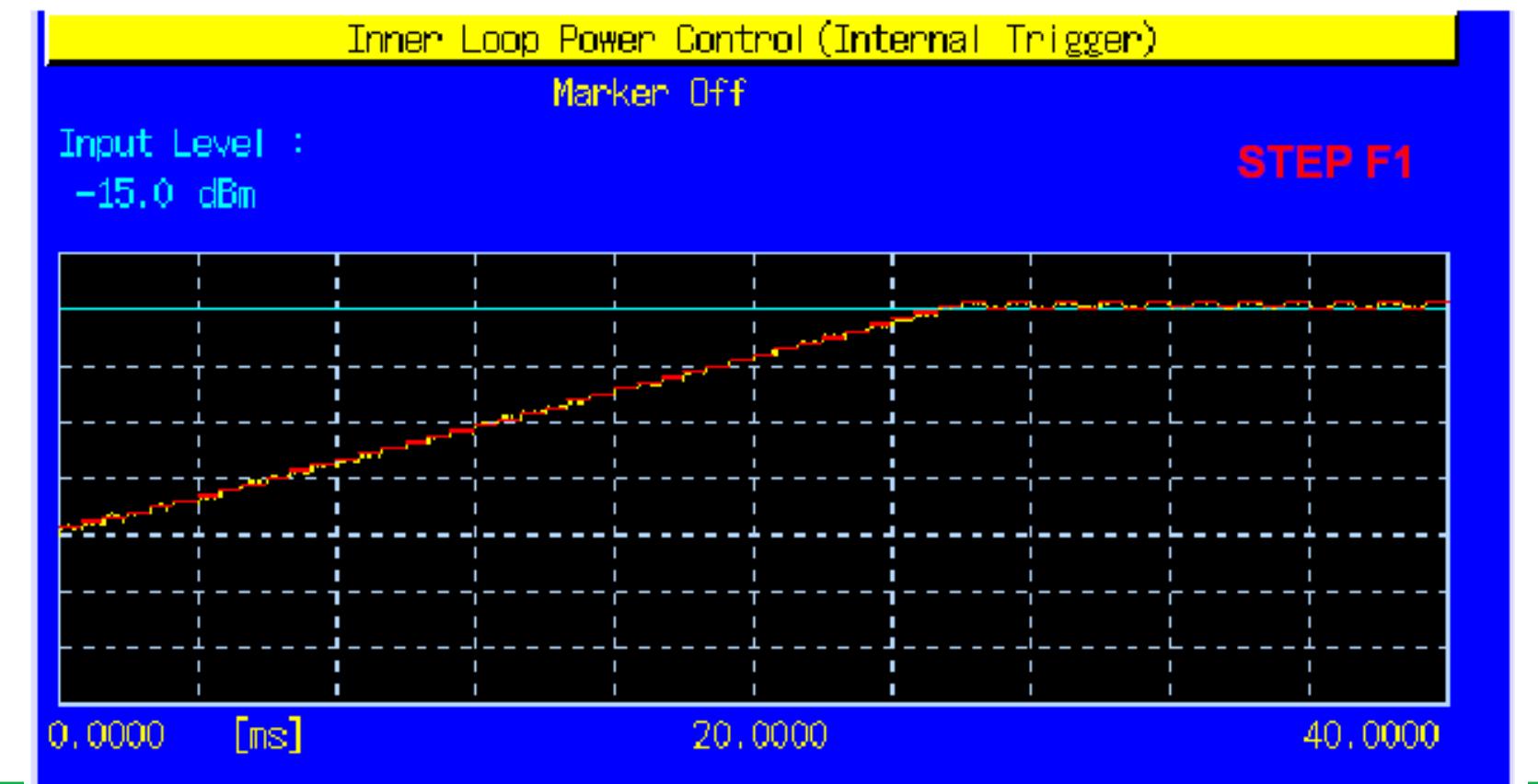
内环功率控制测试(stepE2)



内环功率控制测试(stepF1)

47. set TPC Test Step to F.
48. set the number of slots in Test Step F to 40.
49. set TPC Algorithm to 1.
50. set TPC Step Size to 1dB.
51. set TPC Pattern to Alternate.
52. set Input Level to -15.0dBm.
53. perform the measurement.
54. read the measurement result.

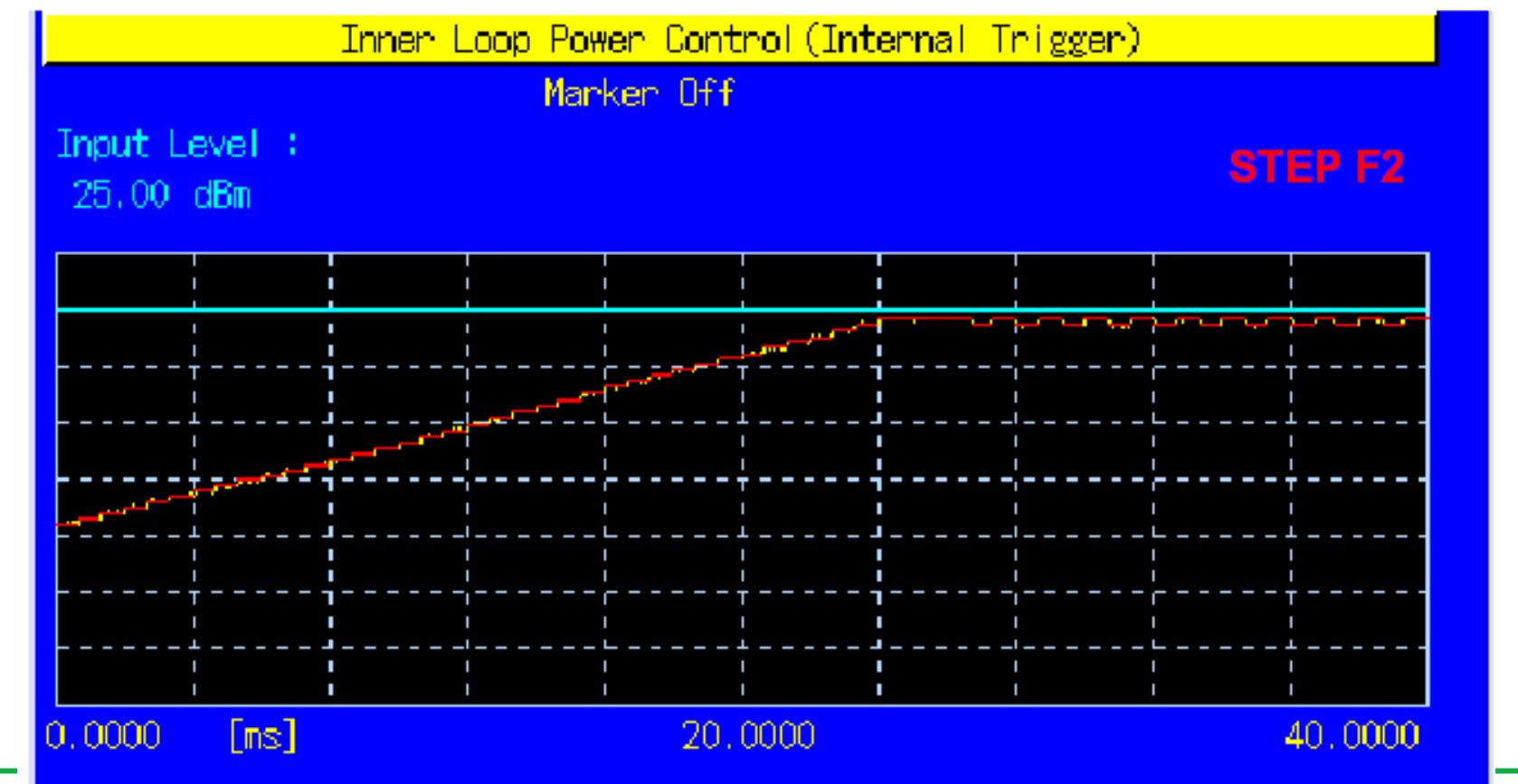
内环功率控制测试(stepF1)



内环功率控制测试(stepF2)

55. set TPC Test Step to F.
56. set the number of slots in Test Step F to 40.
57. set TPC Algorithm to 1.
58. set TPC Step Size to 1dB.
59. set TPC Pattern to Alternate.
60. set Input Level to +25.0dBm.
61. perform the measurement.
62. read the measurement result.

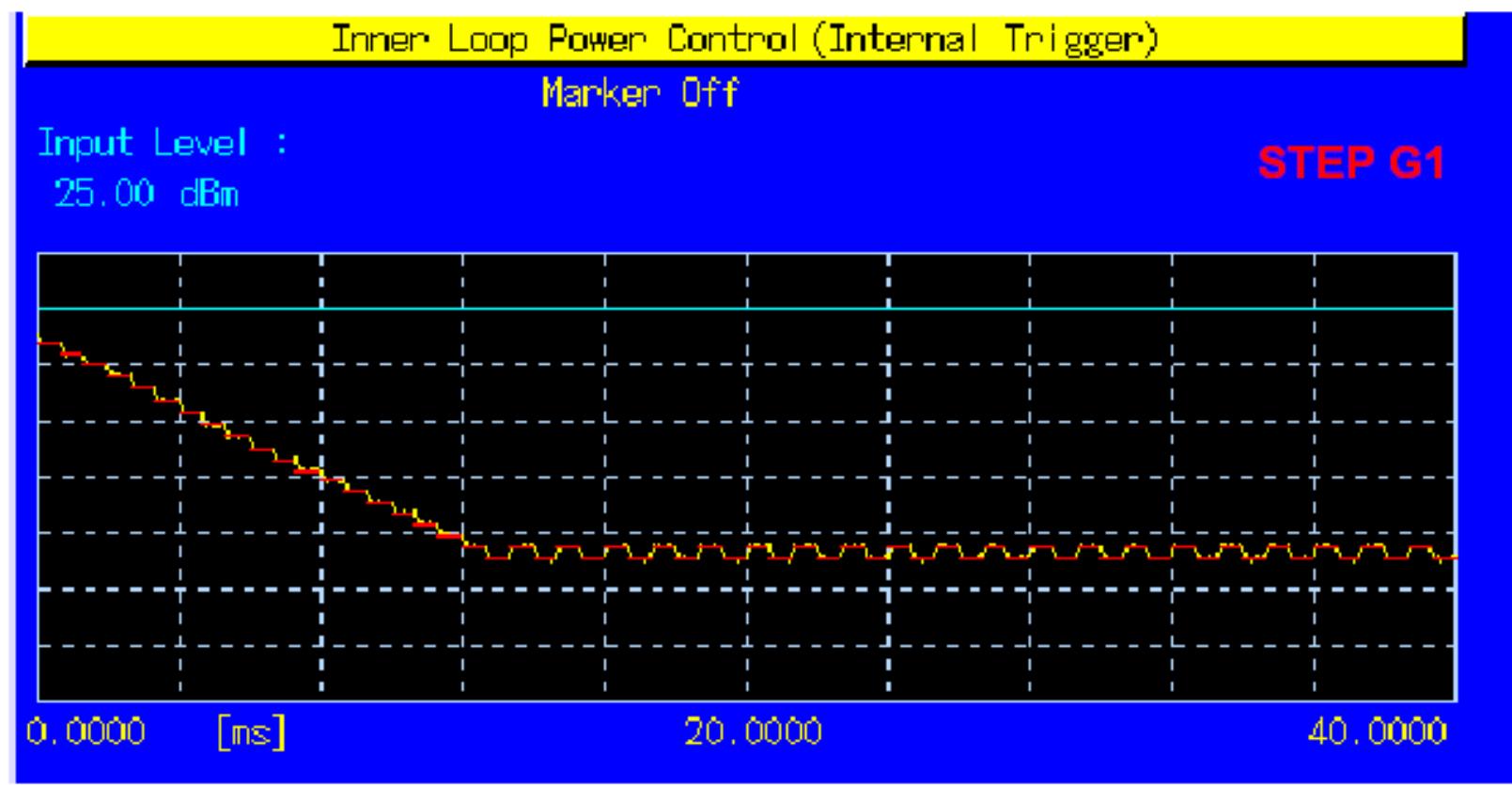
内环功率控制测试(stepF2)



内环功率控制测试(stepG1)

63. set TPC Test Step to G.
64. set the number of slots in Test Step G to 20.
65. set TPC Algorithm to 1.
66. set TPC Step Size to 2dB.
67. set TPC Pattern to Alternate.
68. set Input Level to +25.0dBm.
69. perform the measurement.
70. read the measurement result.

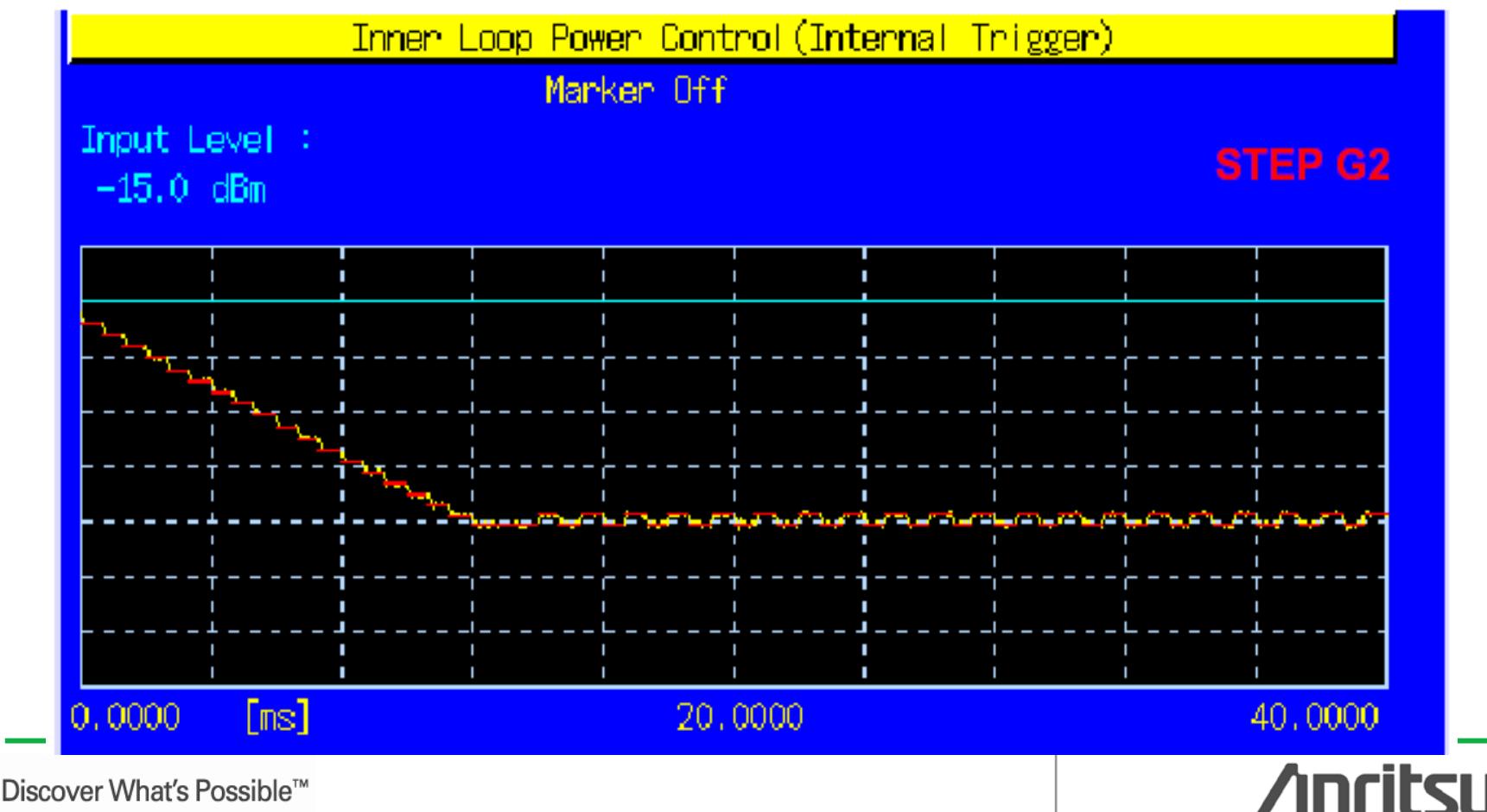
内环功率控制测试(stepG1)



内环功率控制测试(stepG2)

71. set TPC Test Step to G.
72. set the number of slots in Test Step G to 20.
73. set TPC Algorithm to 1.
74. set TPC Step Size to 2dB.
75. set TPC Pattern to Alternate.
76. set Input Level to -15.0dBm.
77. perform the measurement.
78. read the measurement result.

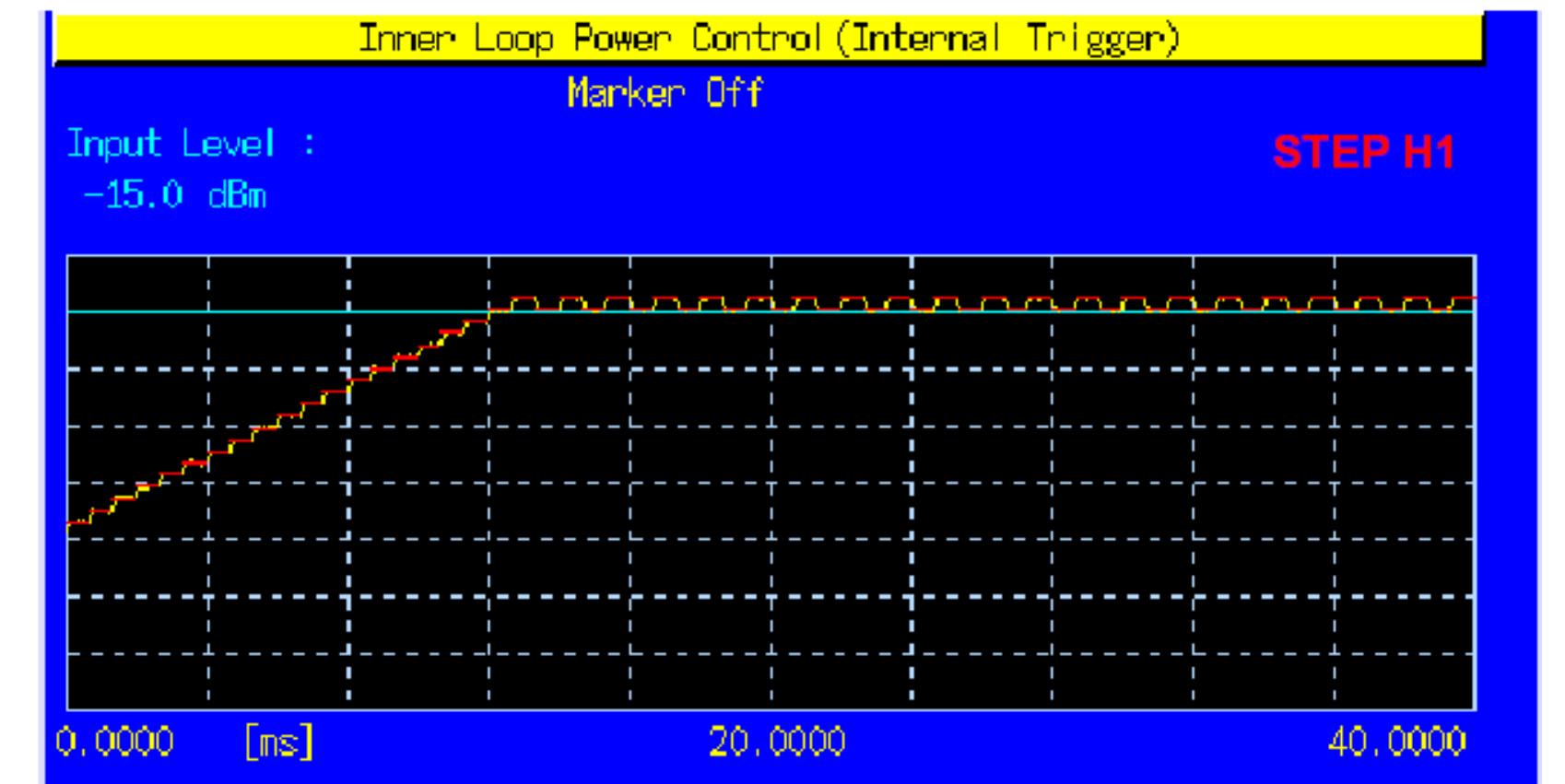
内环功率控制测试(stepG2)



内环功率控制测试(stepH1)

79. set TPC Test Step to H.
80. set the number of slots in Test Step H to 20.
81. set TPC Algorithm to 1.
82. set TPC Step Size to 2dB.
83. set TPC Pattern to Alternate.
84. set Input Level to -15.0dBm.
85. perform the measurement.
86. read the measurement result.

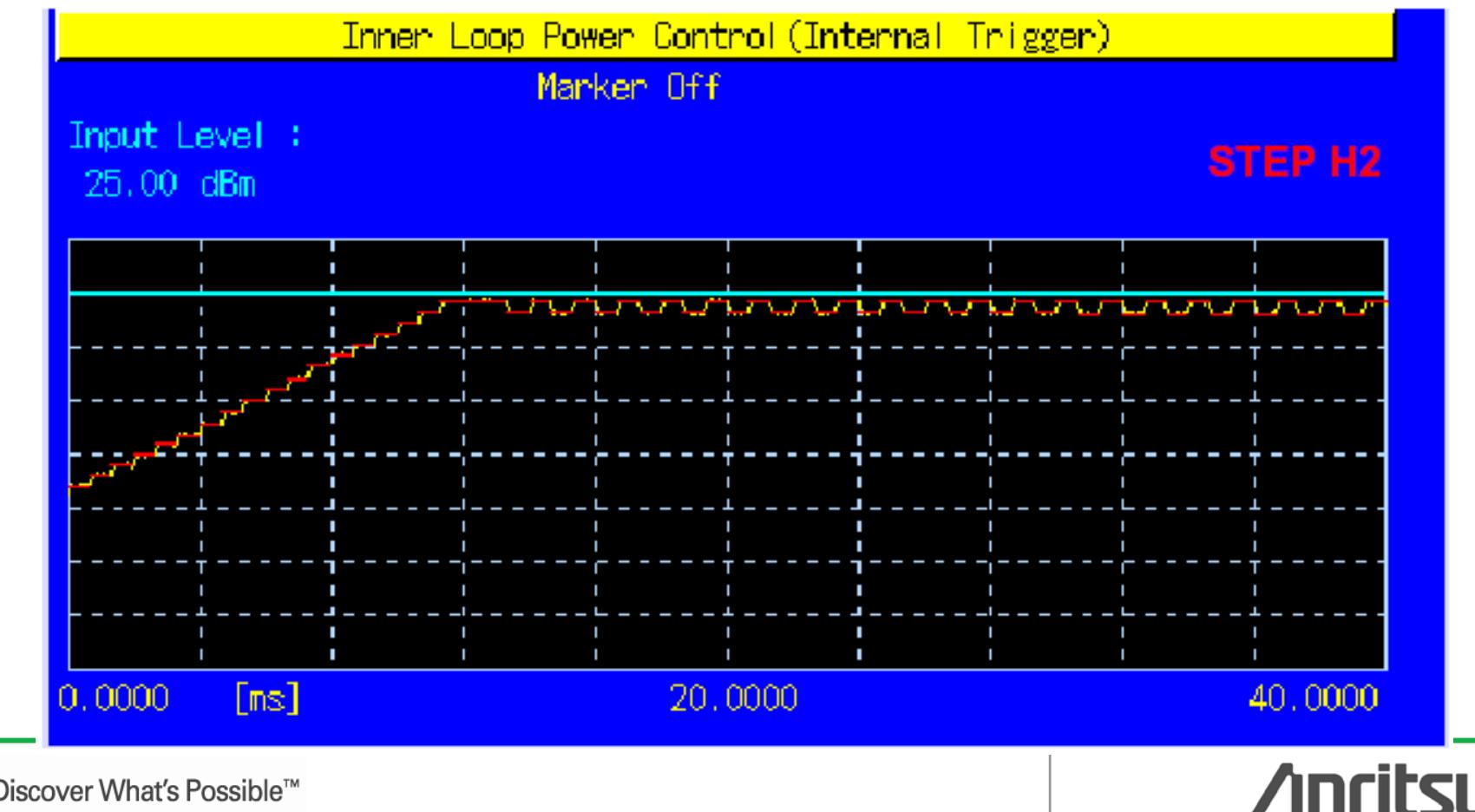
内环功率控制测试(stepH1)



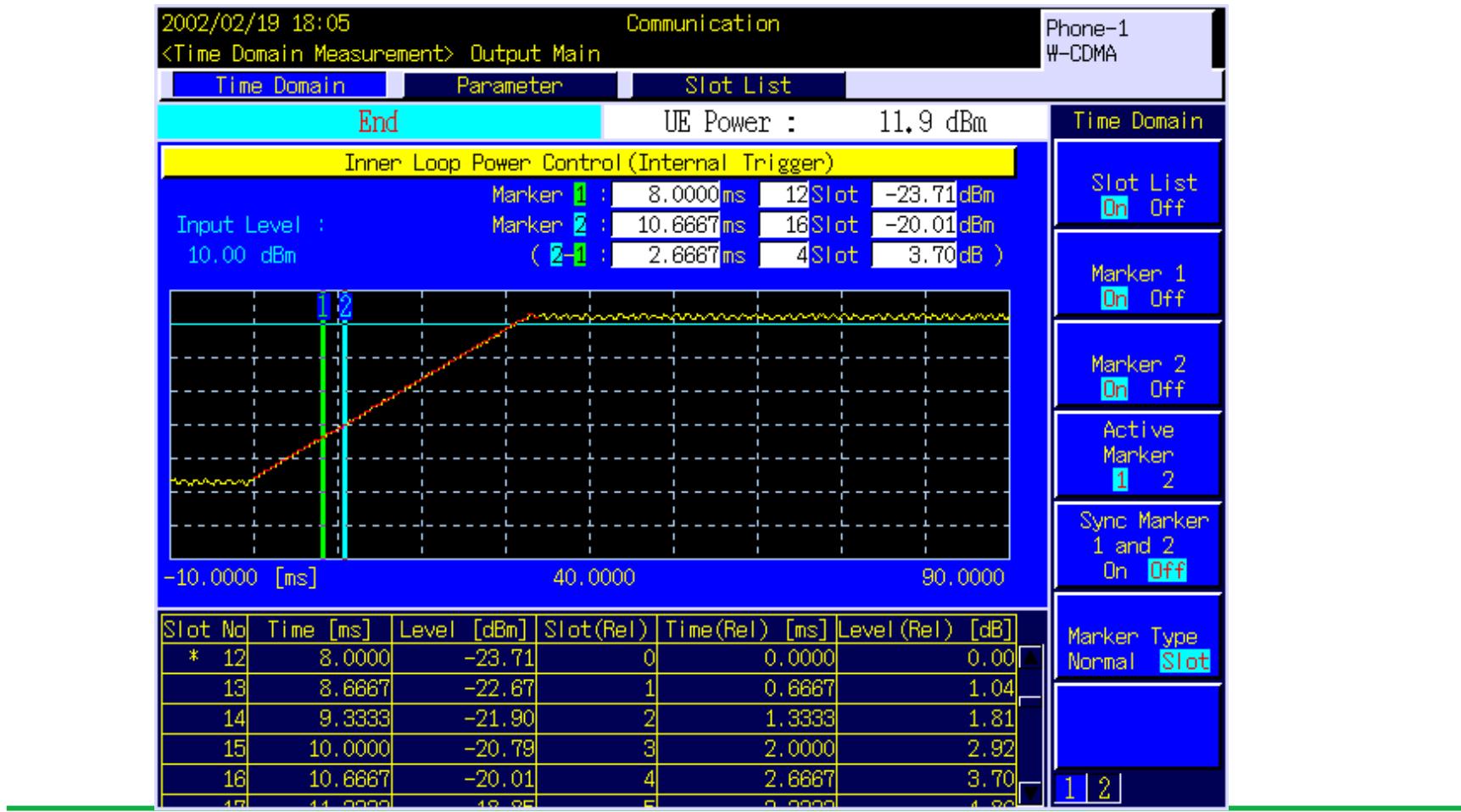
内环功率控制测试(stepH2)

87. set TPC Test Step to H.
88. set the number of slots in Test Step H to 75.
89. set TPC Algorithm to 1.
90. set TPC Step Size to 2dB.
91. set TPC Pattern to Alternate.
92. set Input Level to +25.0dBm.
93. perform the measurement.
94. read the measurement result.

内环功率控制测试(stepH2)



Slot list



Discover What's Possible™

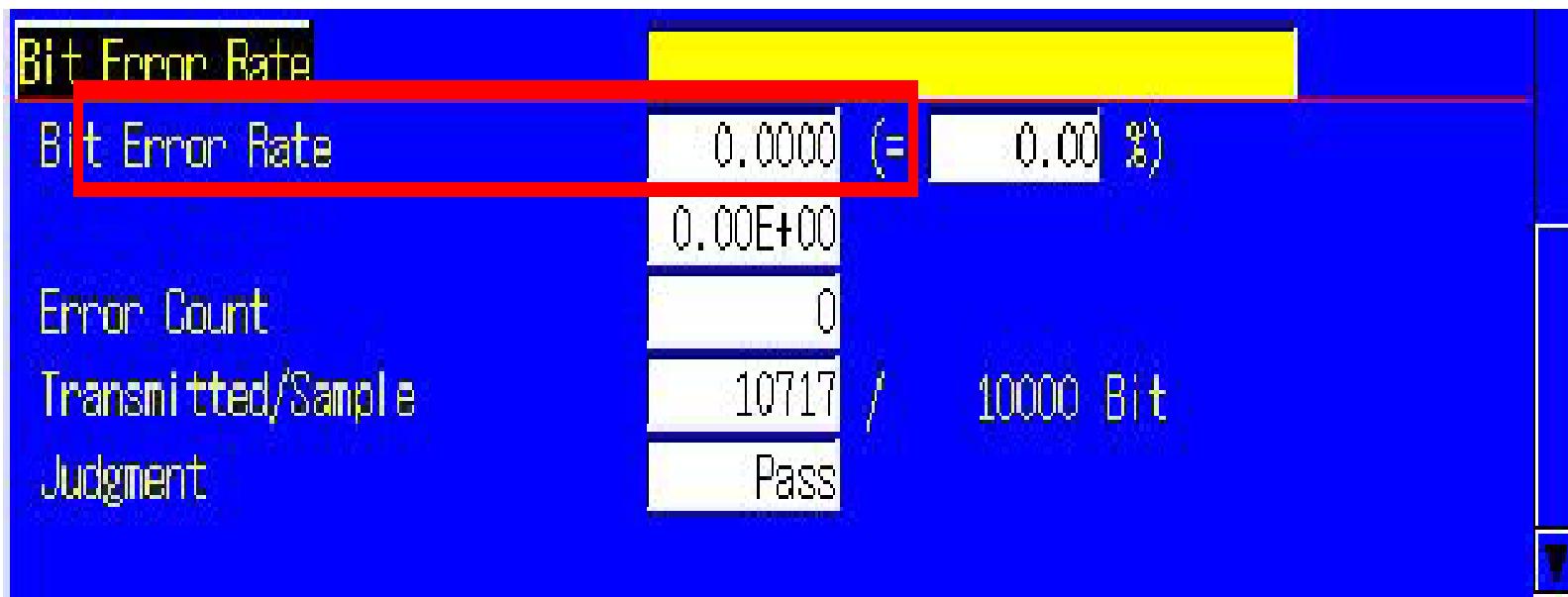
78

Anritsu

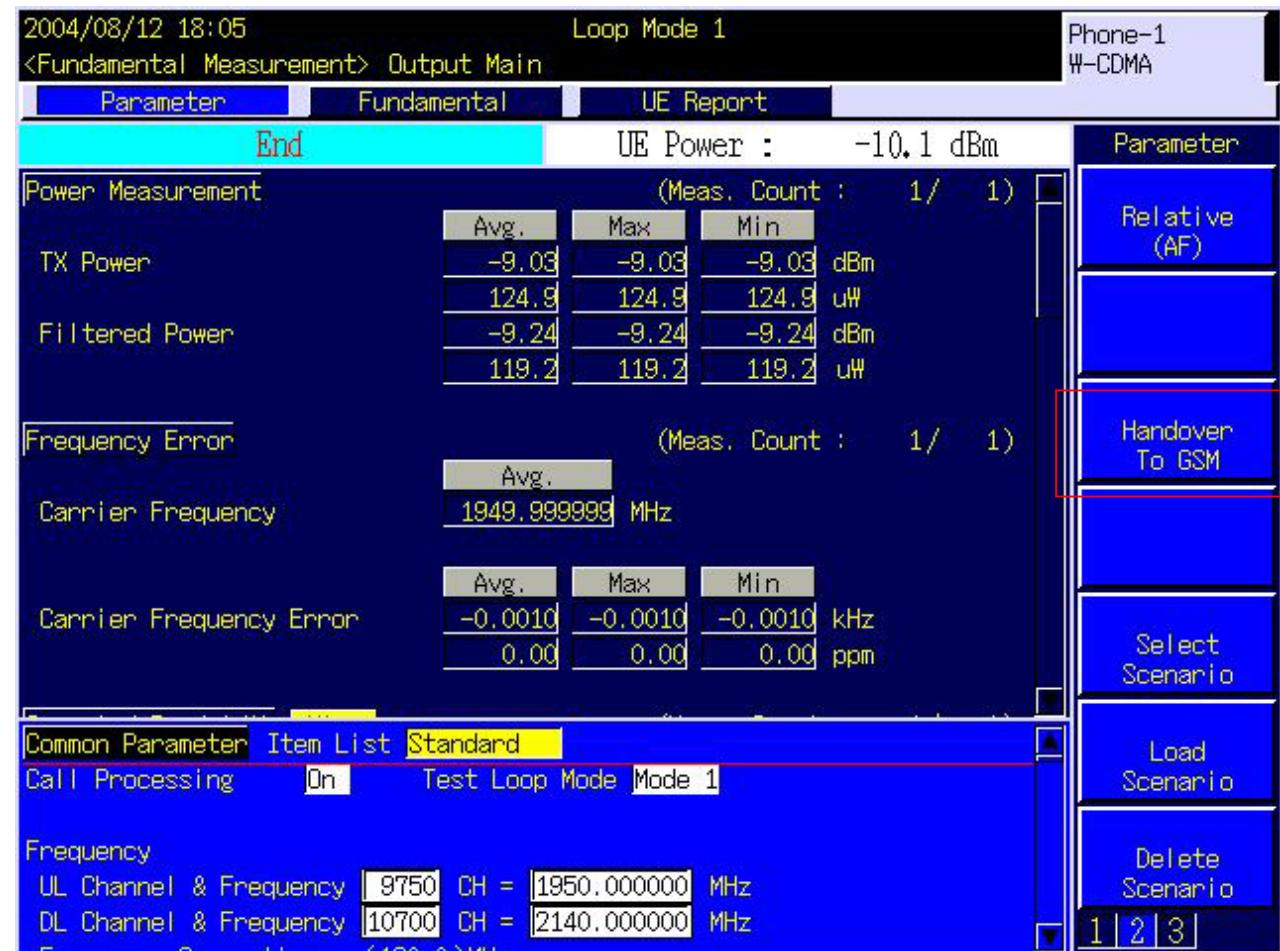
最大输入电平测试 (RX)

1. set TPC Algorithm to 2.
2. set TPC Step Size to 1dB.
3. Connect to Test Loop Mode1.
4. set Output Level to -25.7dBm.
5. set DPCH_Ec/Ior to -19.0dB.
6. set Input Level to +20.0dBm.
7. set TPC Pattern to Inner Loop Power Control.
8. Set BER Measurement to On.
9. set the number of BER measurement samples to 10000 bits.
10. perform BER measurement.
11. read the result of BER measurement.

最大输入电平测试



从WCDMA切换到 GSM



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Thank You !